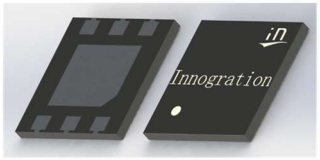
Gallium Nitride 50V, 16W, DC-6GHz RF Power Transistor

Description

STAV58016P2



The STAV58016P2 is a 16 watt, unmatched GaN HEMT, ideal for general applications up to 6GHz

It features high gain, wide band and low cost, in 4*4.5mm DFN plastic package.

It can support CW, pulse or any modulated signal.

There is no guarantee of performance when this part is used outside of stated frequencies.

- Typical Class AB Single--Carrier W--CDMA Characterization Performance:

V_{DD} = 50 Vdc, I_{DQ} = 20 mA, Input Signal PAR = 10 dB @ 0.01% Probability on CCDF.

(On innogration application board with device soldered)

Freq (MHz)	Pout (dBm)	CCDF (dB)	Ppeak (dBm)	Ppeak (W)	ACPR (dBc)	Gain (dB)	Eff (%)
3300	33.00	9.12	42.12	16.3	-35.9	16.8	27.8
3400	32.99	9.04	42.03	16.0	-36.3	17.1	27.8
3500	33.00	8.92	41.92	15.6	-37.1	17.4	28.1
3600	32.97	8.90	41.88	15.4	-38.1	17.5	27.4
3700	33.00	8.97	41.97	15.7	-39.4	17.4	27.4
3800	33.01	8.90	41.91	15.5	-40.7	16.6	25.7

Applications

- 5G, 4G wireless infrastructure
- Wideband or narrowband power amplifier
- Test instruments
- Civil pulse radar
- Jammer

Important Note: Proper Biasing Sequence for GaN HEMT Transistors

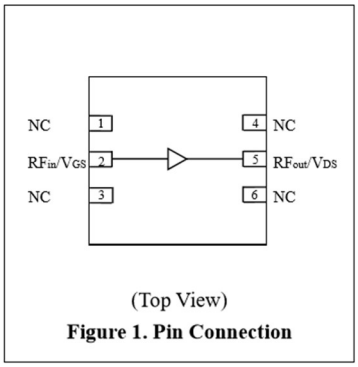
Turning the device ON

1. Set VGS to the pinch--off (VP) voltage, typically –5 V
2. Turn on VDS to nominal supply voltage
3. Increase VGS until IDS current is attained
4. Apply RF input power to desired level

Turning the device OFF

1. Turn RF power off
2. Reduce VGS down to VP, typically –5 V
3. Reduce VDS down to 0 V
4. Turn off VGS

Figure 1: Pin Connection definition



(Top View)

Figure 1. Pin Connection

Table 1. Maximum Ratings

Rating	Symbol	Value	Unit
Drain--Source Voltage	V_{DSS}	+200	Vdc
Gate--Source Voltage	V_{GS}	-8 to +0.5	Vdc
Maximum forward gate current	Igs	2	mA
Operating Voltage	V_{DD}	55	Vdc
Storage Temperature Range	Tstg	-65 to +150	°C
Case Operating Temperature	T_C	+150	°C
Operating Junction Temperature	T_J	+225	°C

Table 2. Thermal Characteristics

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Case by FEA T_C= 85°C, Pdiss=6W at Pavg=33dBm WCDMA 1 carrier	$R_{\theta JC}$	10	°C /W

Table 3. Electrical Characteristics (TA = 25℃ unless otherwise noted)

DC Characteristics (measured on wafer prior to packaging)

Characteristic	Conditions	Symbol	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	VGS=-8V; IDS=2mA	V_{DSS}		200		V
Gate Threshold Voltage	VDS =10V, ID = 2mA	$V_{GS(th)}$	-4	-2.9	-2	V
Gate Quiescent Voltage	VDS =50V, IDS=20mA, Measured in Functional Test	$V_{GS(Q)}$		-3		V

Ruggedness Characteristics

Characteristic	Conditions	Symbol	Min	Typ	Max	Unit
Load mismatch capability	3.6GHz, Pout=33dBm WCDMA 1 Carrier, All phase, No device damages	VSWR		10:1		

Figure 2: Median Lifetime vs. Channel Temperature

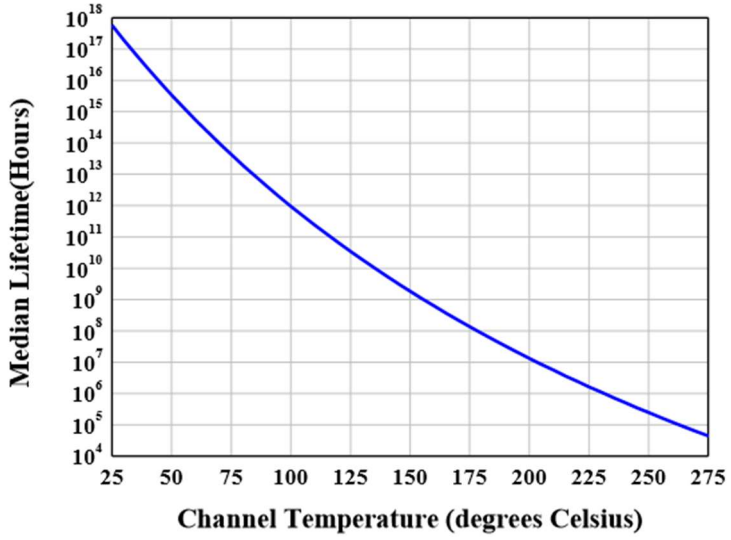
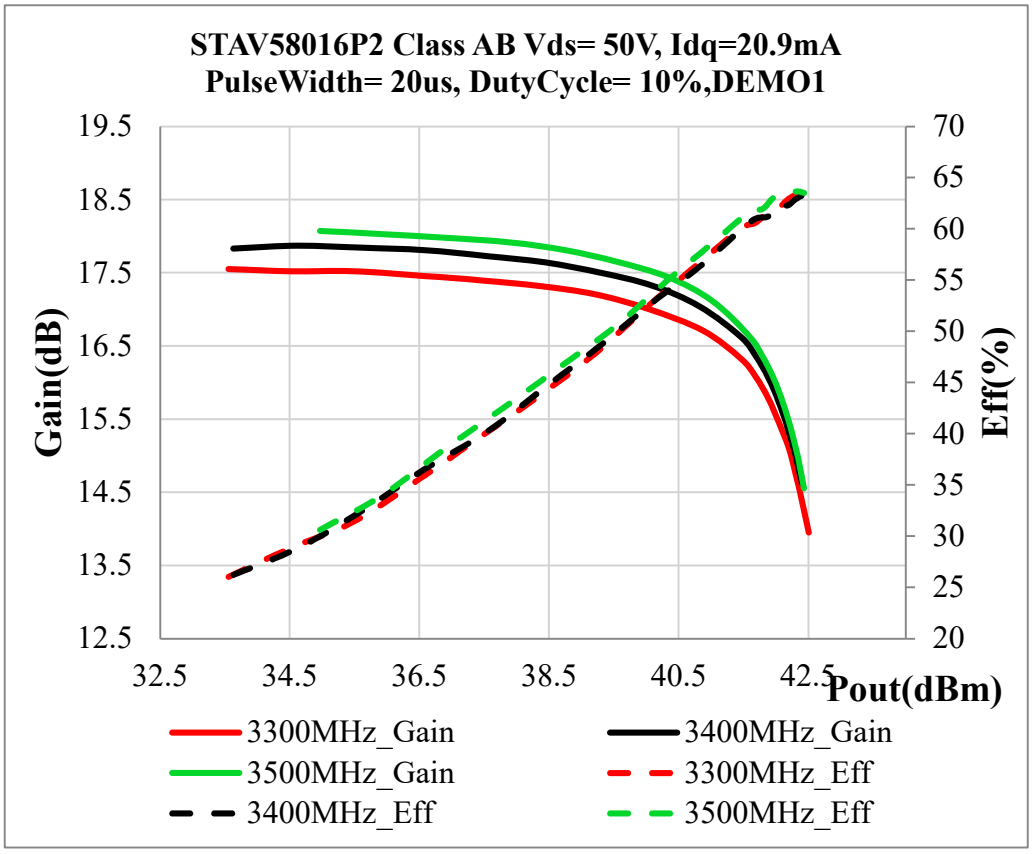


Figure 3: Efficiency and power gain as function of Pout



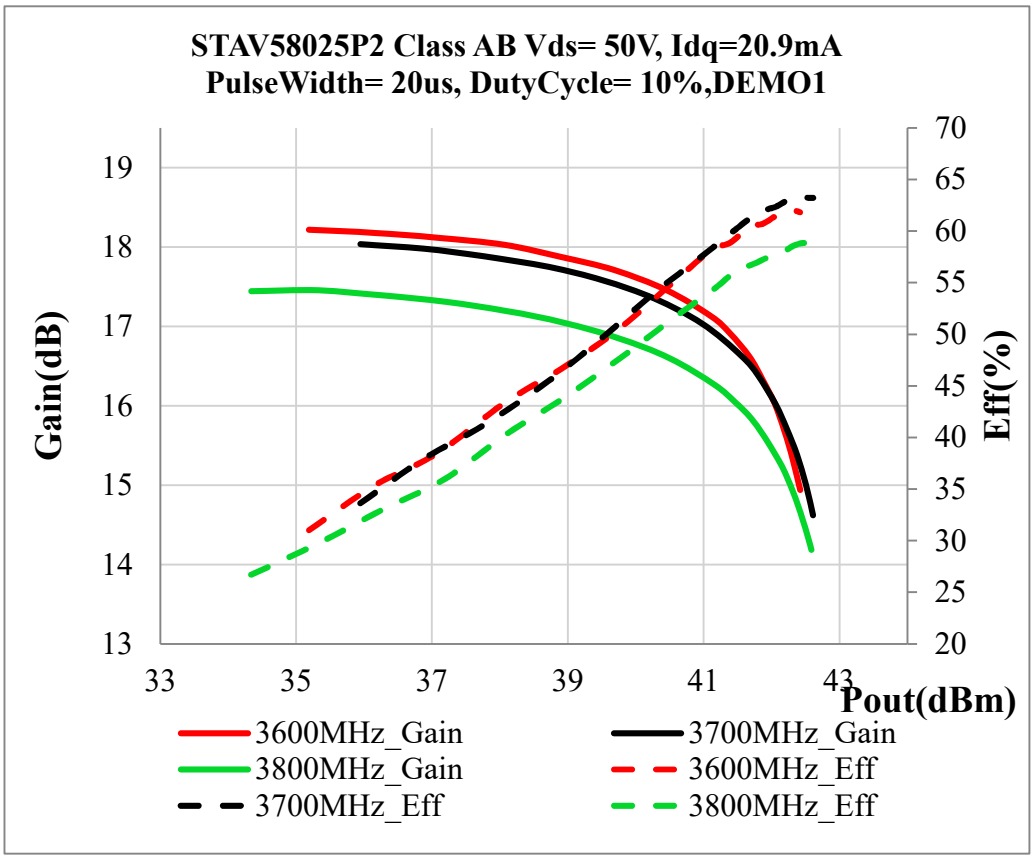


Figure 4: Network analyzer output, S11 and S21

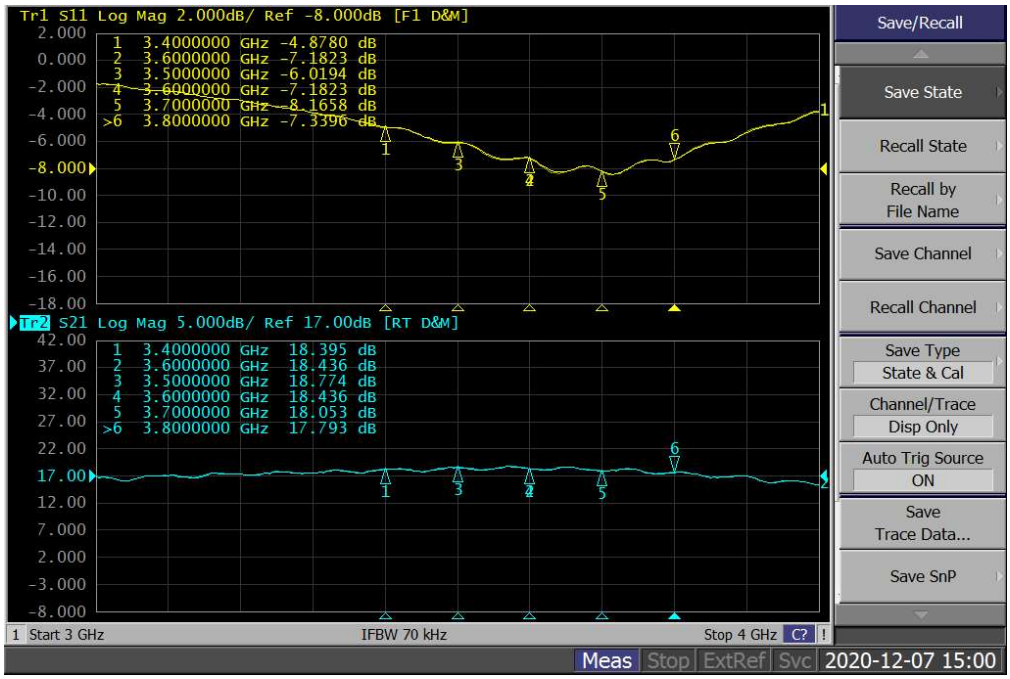
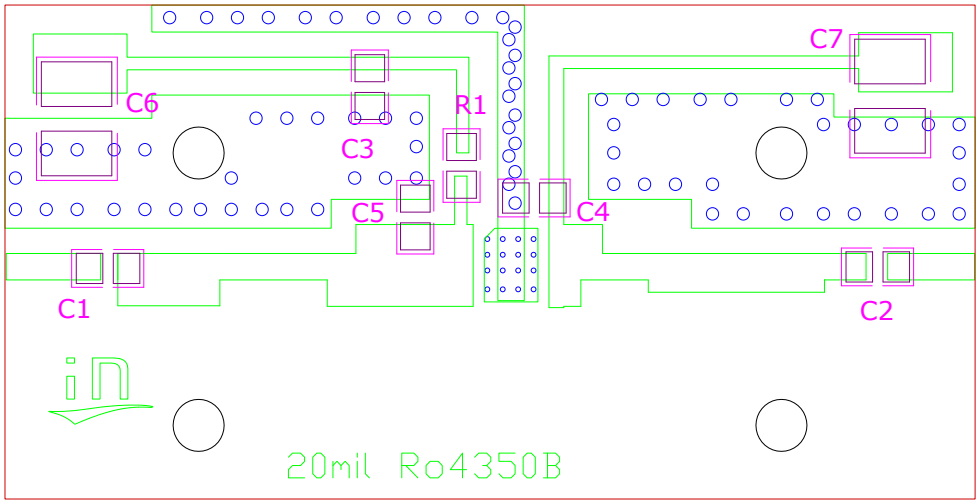


Figure 5: Picture of application board

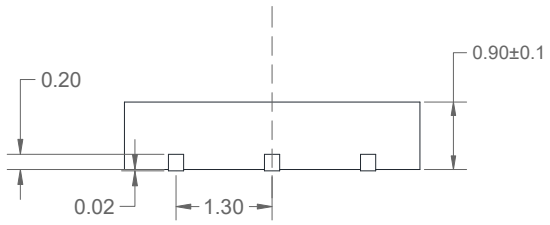


1. Table 4. Bill of materials of application board (PCB layout upon request, RO4350B 20mils)

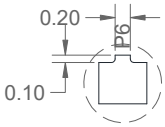
Designator	Comment	Footprint	Quantity
C1, C2, C3, C4	8.2pF	0603	4
C5	1.1pF	0603	1
C6, C7	10uF/100V	1210	2
R1	10ohm	0603	1

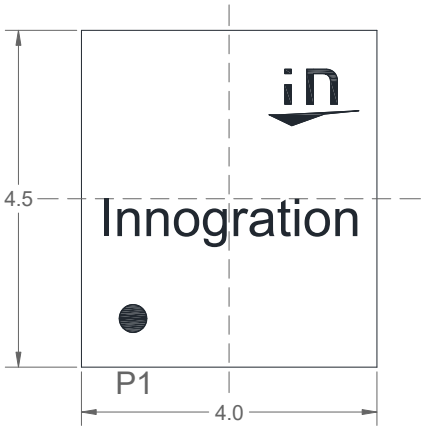
Package Dimensions

4.0*4.5mm Plastic Package

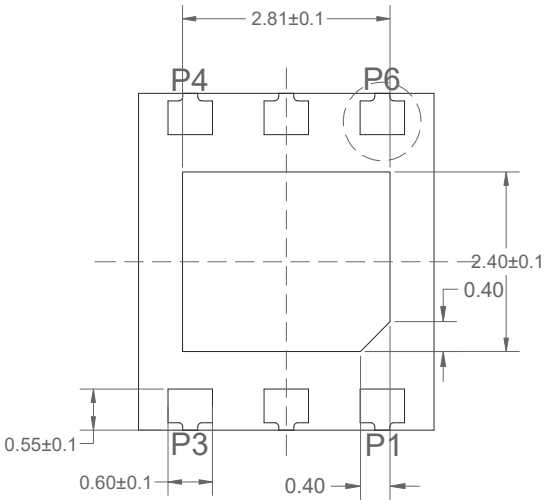


Front View

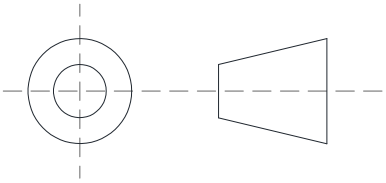




Top View



Bottom View



Unit: mm

Notes:

1. All dimensions are in mm;
2. The tolerances unless specified are ±0.1mm.

Revision history

Table 4. Document revision history

Date	Revision	Datasheet Status
2020/6/1	**V1.0**	**Preliminary Datasheet Creation**

Application data based on LSM-10-27

Notice

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