Gallium Nitride 50V, 240W, 3.3-3.6GHz RF Power Transistor

Description

The STAV36240AY2 is an input matched, single ended 240watt, GaN HEMT, ideal for 5G applications from 3.3 to 3.6GHz.

There is no guarantee of performance when this part is used outside of stated frequencies.

• WCMDA 3GPP TM1 64 DPCH 9.9 dB PAR @ 0.01% CCDF. VDS = 50 V, IDQ = 200 mA,

(On innogration Class AB application board with device soldered)

STAV36240AY2

Freq(MHz)	Pout(dBm)	CCDF(dB	Ppeak(dBm)	Ppeak(W)	ACPR(dBc)	Gain(dB)	Efficiency(%)
3400	47.43	7.02	54.45	278.82	-35.01	13.03	29.93
3500	47.46	6.79	54.25	266.30	-33.83	13.28	31.37
3600	47.48	6.56	54.04	253.57	-32.85	13.14	32.95

Applications

- Sub-4GHz pulse or CW amplifier
- 5G base station amplifier
- Doherty power amplifier

Important Note: Proper Biasing Sequence for GaN HEMT Transistors

Turning the device ON

- 1. Set VGS to the pinch--off (VP) voltage, typically -5 V
- 2. Turn on VDS to nominal supply voltage
- 3. Increase VGS until IDS current is attained
- 4. Apply RF input power to desired level

Turning the device OFF

- 1. Turn RF power off
- 2. Reduce VGS down to VP, typically -5 V
- 3. Reduce VDS down to 0 V
- 4. Turn off VGS

Figure 1: Pin Connection definition

Transparent top view (Backside grounding for source)

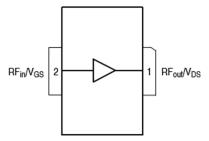


Table 1. Maximum Ratings

Rating	Symbol	Value	Unit
DrainSource Voltage	V _{DSS}	+200	Vdc
GateSource Voltage	V _{GS}	-8 to +0.5	Vdc
Operating Voltage	V _{DD}	55	Vdc
Maximum gate current	lgs	30	mA
Storage Temperature Range	Tstg	-65 to +150	°C
Case Operating Temperature	Tc	+150	°C

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Operating Junction Temperature	TJ	+225	°C
Table 2. Thermal Characteristics			
Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Case by FEA	Rejc	TBD	°C /W

Tc= 85°C	at Pd=70W
10-00-0,	

Table 3. Electrical Characteristics (TA = 25℃ unless otherwise noted)

DC Characteristics (Each path, measured on wafer prior to packaging)

Characteristic	Conditions	Symbol	Min	Тур	Max	Unit
Drain-Source Breakdown Voltage	VGS=-8V; IDS=30mA	V _{DSS}		200		V
Gate Threshold Voltage	VDS =10V, ID = 30mA	V _{GS(th)}	-4		-2	V
Gate Quiescent Voltage VDS =50V, IDS=200mA, Measured in Functional Test		V _{GS(Q)}		-3.24		V

Ruggedness Characteristics

Characteristic	Conditions	Symbol	Min	Тур	Max	Unit
Load mismatch capability	3.5GHz, Pout=240W pulse CW for each path All phase, No device damages	VSWR		10:1		

Figure 2: Median Lifetime vs. Channel Temperature

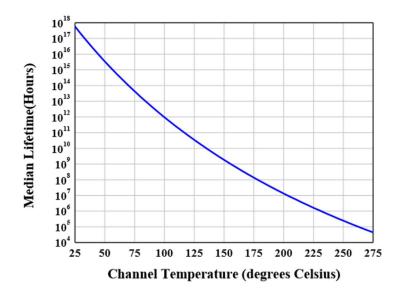


Figure 3: Efficiency and power gain as function of Pout

(VDD = 50Vdc, IDQ = 200 mA, Pulse width=20us, duty cycle=10%)

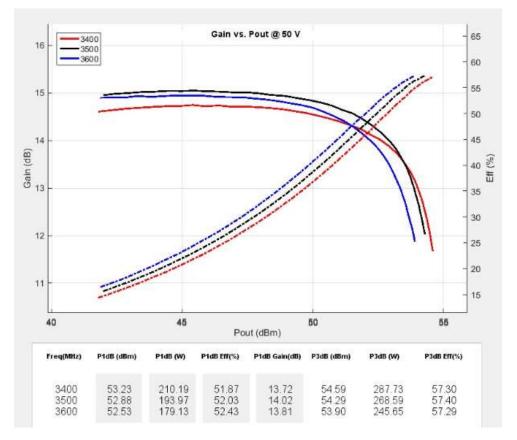
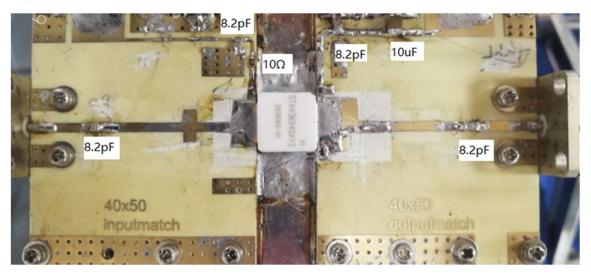


Figure 4: S11 / S21 output from network analyzer

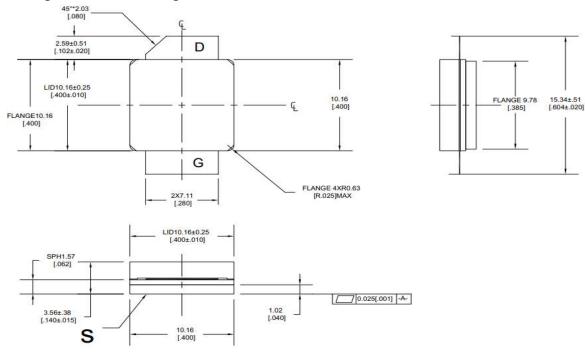


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Figure 5: Layout and BOM of Application board 3.4-3.6GHz Class AB (RO4350B 30mils)



Earless Flanged Ceramic Package; 2 leads



Unit: mm [inch] Tolerance .xx +/- 0.01 .xxx +/- 0.005 inches

Revision history

Table 4. Document revision history

Date	Revision	Datasheet Status
2020/6/28	V1.0	Preliminary Datasheet Creation

Application data based on: LSM-20-19

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