



Gallium Nitride 50V, 360W, 3.6-4.2GHz RF Power Transistor

Description

The STBV40360BY4V is a 360-watt, internally matched GaN HEMT, designed for 5G cellular applications with frequencies from 3.6-4.2GHz, **enabled by wide band VBW capability to support IBW ≥ 200MHz.**

It can be configured as asymmetrical Doherty for 4G or 5G application, delivering 55W average power, according to normal 8.5dB back off.

There is no guarantee of performance when this part is used in applications designed Outside of these frequencies.

- Typical Doherty Pulsed CW and 1C W--CDMA Characterization Performance:

$V_{DD} = 50 \text{ Vdc}$, $I_{DQA} = 210 \text{ mA}$, $V_{GSB} = -5.5 \text{ Vdc}$,

(1) Pulsed condition: 20us and 10%, $P_{sat} = P5dB$

(2) 1C WCDMA; Signal PAR = 10 dB @ 0.01% Probability on CCDF.

3.6-4.0GHz

Freq (GHz)	Pulse CW Signal(1)			Pavg=47.0dBm WCDMA Signal(2)		
	Gain_P1 (dB)	Psat (dBm)	Psat (W)	Gp (dB)	Eff (%)	ACPR (dBc)
3.6	11.85	56.07	405	12.06	46.0	-28.29
3.7	11.83	56.06	404	12.17	46.1	-29.95
3.8	12.13	56.04	402	12.16	45.7	-32.33
3.9	12.34	55.84	383	11.92	46.1	-34.71
4.0	12.71	55.60	363	11.73	45.3	-36.06

3.8-4.2GHz

Freq (GHz)	Pulse CW Signal(1)				Pavg=47.0dBm WCDMA Signal(2)		
	P3 (dBm)	P3 (W)	Psat (dBm)	Psat (W)	Gp (dB)	D (%)	ACPR5M (dBc)
3.8	55.60	363	56.01	399	11.24	41.62	-27.62
3.9	54.92	310	55.95	393	11.15	43.30	-29.01
4.0	55.10	323	55.90	389	11.41	43.02	-31.28
4.1	55.53	357	55.90	389	11.40	42.62	-30.18
4.2	55.51	355	55.70	372	11.20	41.50	-31.51

Applications

- Asymmetrical Doherty amplifier within N77 5G band and S band power amplifier

Important Note: Proper Biasing Sequence for GaN HEMT Transistors

Turning the device ON

1. Set VGS to the pinch-off (VP) voltage, typically -5 V
2. Turn on VDS to nominal supply voltage
3. Increase VGS until IDS current is attained
4. Apply RF input power to desired level

Turning the device OFF

1. Turn RF power off
2. Reduce VGS down to VP, typically -5 V
3. Reduce VDS down to 0 V
4. Turn off VGS

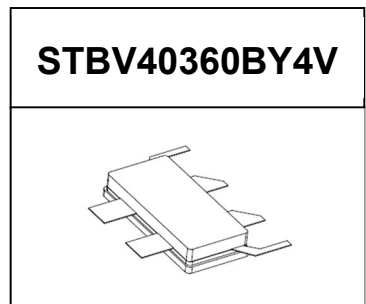


Figure 1: Pin Connection definition

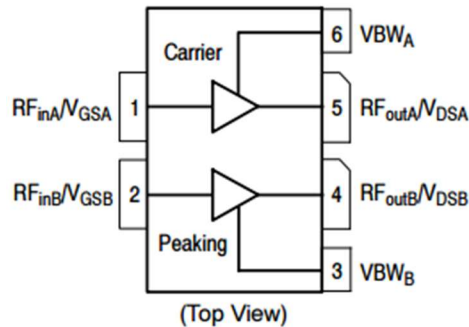


Table 1. Maximum Ratings

Rating	Symbol	Value	Unit
Drain—Source Voltage	V_{DSS}	+200	Vdc
Gate—Source Voltage	V_{GS}	-8 to +0.5	Vdc
Operating Voltage	V_{DD}	55	Vdc
Maximum gate current	I_{gs}	51	mA
Storage Temperature Range	T_{stg}	-65 to +150	°C
Case Operating Temperature	T_c	+150	°C
Operating Junction Temperature	T_j	+225	°C

Table 2. Thermal Characteristics

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Case by FEA $T_c = 85^\circ\text{C}$, $P_{out} = 50\text{W}$, 3.9GHz Doherty application board	$R_{\theta JC}$	1.6	°C /W

Table 3. Electrical Characteristics (TA = 25°C unless otherwise noted)

DC Characteristics (main path, measured on wafer prior to packaging)

Characteristic	Conditions	Symbol	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$V_{GS} = -8\text{V}$; $I_{DS} = 17\text{mA}$	V_{DSS}		200		V
Gate Threshold Voltage	$V_{DS} = 10\text{V}$, $I_D = 17\text{mA}$	$V_{GS(th)}$	-4		-2	V
Gate Quiescent Voltage	$V_{DS} = 50\text{V}$, $I_{DS} = 210\text{mA}$, Measured in Functional Test	$V_{GS(Q)}$		-3.24		V

DC Characteristics (peak path, measured on wafer prior to packaging)

Characteristic	Conditions	Symbol	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$V_{GS} = -8\text{V}$; $I_{DS} = 34\text{mA}$	V_{DSS}		200		V
Gate Threshold Voltage	$V_{DS} = 10\text{V}$, $I_D = 34\text{mA}$	$V_{GS(th)}$	-4		-2	V
Gate Quiescent Voltage	$V_{DS} = 50\text{V}$, $I_{DS} = 420\text{mA}$, Measured in Functional Test	$V_{GS(Q)}$		-3.24		V

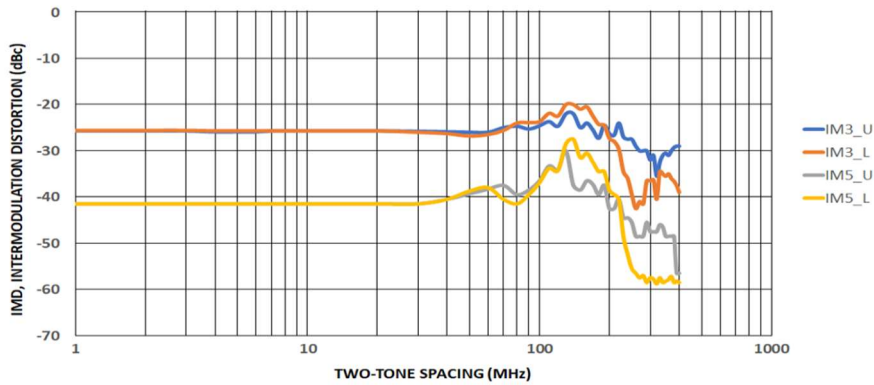
Ruggedness Characteristics

Characteristic	Conditions	Symbol	Min	Typ	Max	Unit
Load mismatch capability	3.9GHz, $P_{out} = 55\text{W}$ WCDMA 1 Carrier in Doherty circuit All phase, No device damages	VSWR		10:1		



Figure 2: Intermodulation Distortion Products versus Two-Tone Spacing

Vdd=50V, Pout=47dBm, Center Frequency=3.9GHz



3.6-4.0GHz

Figure 3: Efficiency and power gain as function of Pout (3.6-4GHz Doherty)

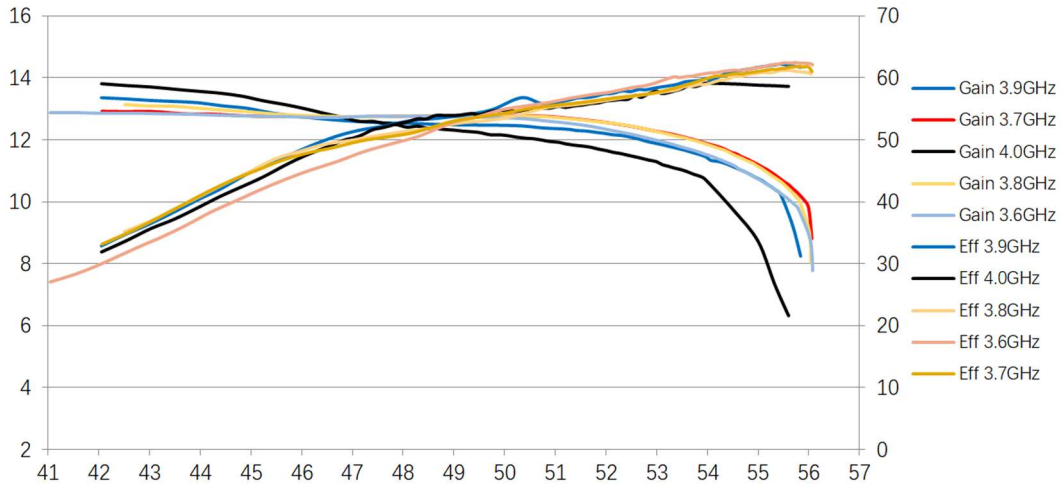


Figure 4: Network analyzer output, S11 and S21 (3.6-4GHz Doherty)

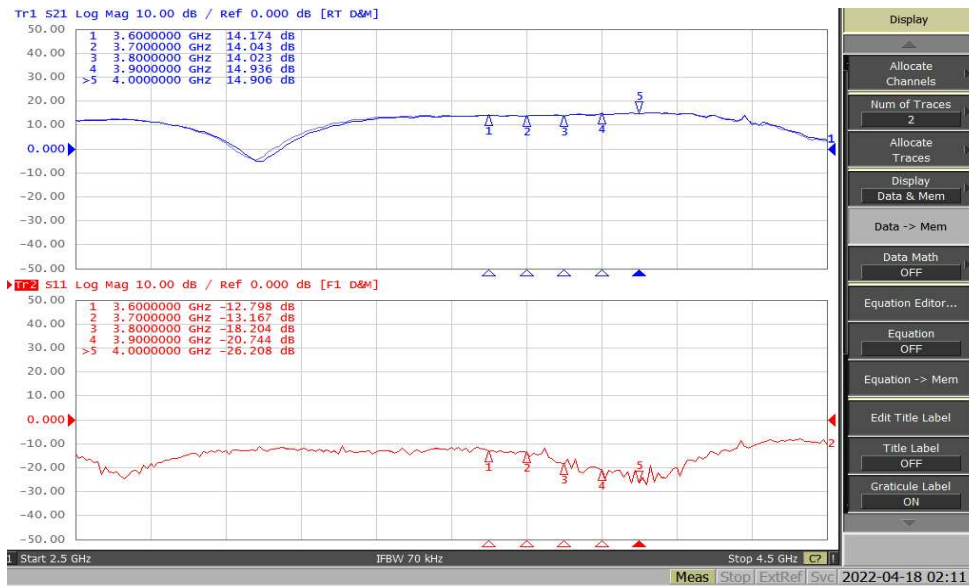


Figure 5: Picture of application board Doherty circuit for 3.6-4GHz

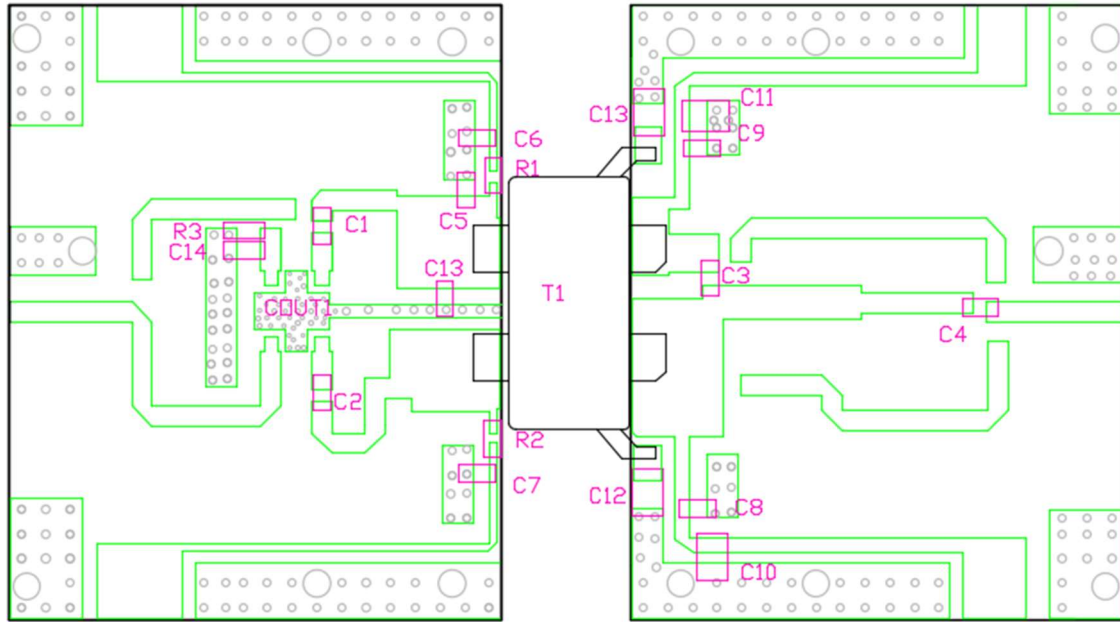


Table 4. Bill of materials of application board (PCB layout upon request, RO4350B 30mils)

Part	Quantity	Description	Part Number	Manufacture
C1,C2,C4,C6, C7,C8,C9	7	8.2pF High Q Capacitor	251SHS8R2BSE	TEMEX
C3	1	0.9pF High Q Capacitor	ATC600S0R9	ATC
C5,C13	2	0.3pF High Q Capacitor	251SHS0R3BSE	TEMEX
C10,C11,C12,C13	4	10uF MLCC	RS80R2A106M	MARUWA
C14	1	0.1pF High Q Capacitor	251SHS0R1BSE	TEMEX
R1,R2	2	5.1 Ω Power Resistor	ESR03EZPF5R10	ROHM
R3	1	51 Ω Power Resistor	S1206N	RN2
COUT1	1	3 dB Bridge	XC3500P-03S	ANAREN
T1	1	360W GaN Dual Transistor	STBV40360BY4V	Innogrations



3.8-4.2GHz

Figure 6: Efficiency and power gain as function of Pout (3.8-4.2GHz Doherty)

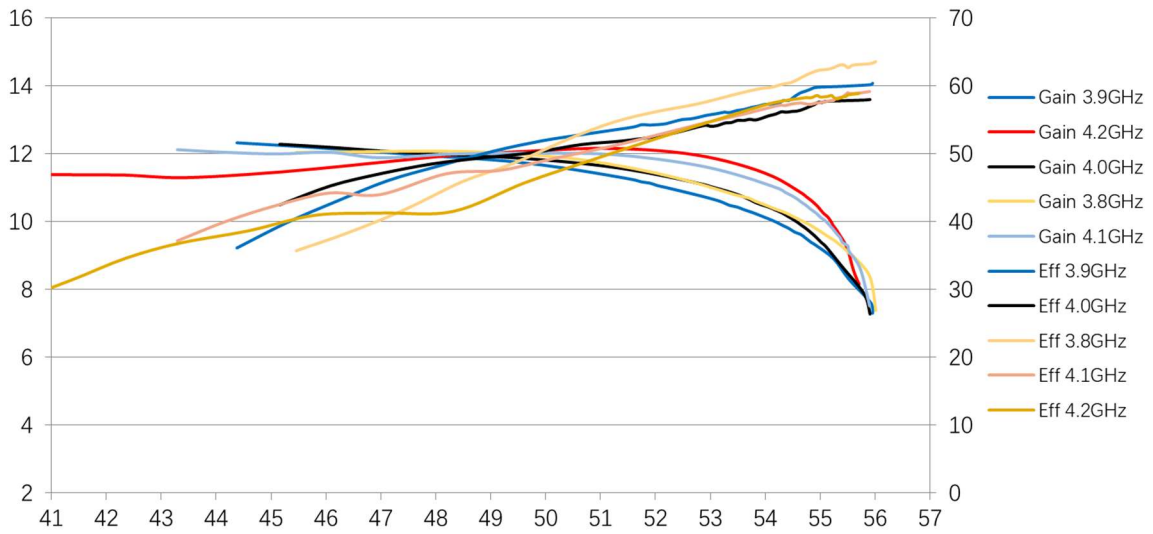


Figure 7: Network analyzer output, S11 and S21 (3.8-4.2GHz Doherty)

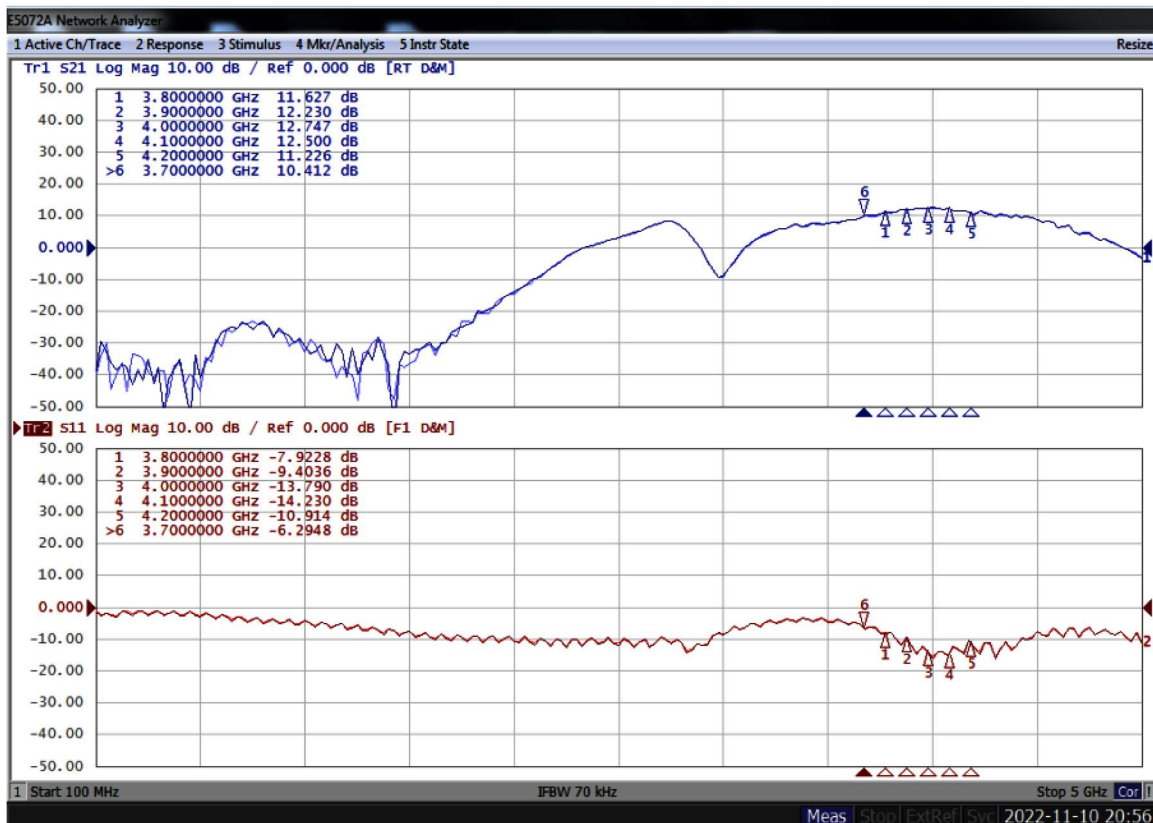


Figure 8: Picture of application board Doherty circuit for 3.8-4.2GHz

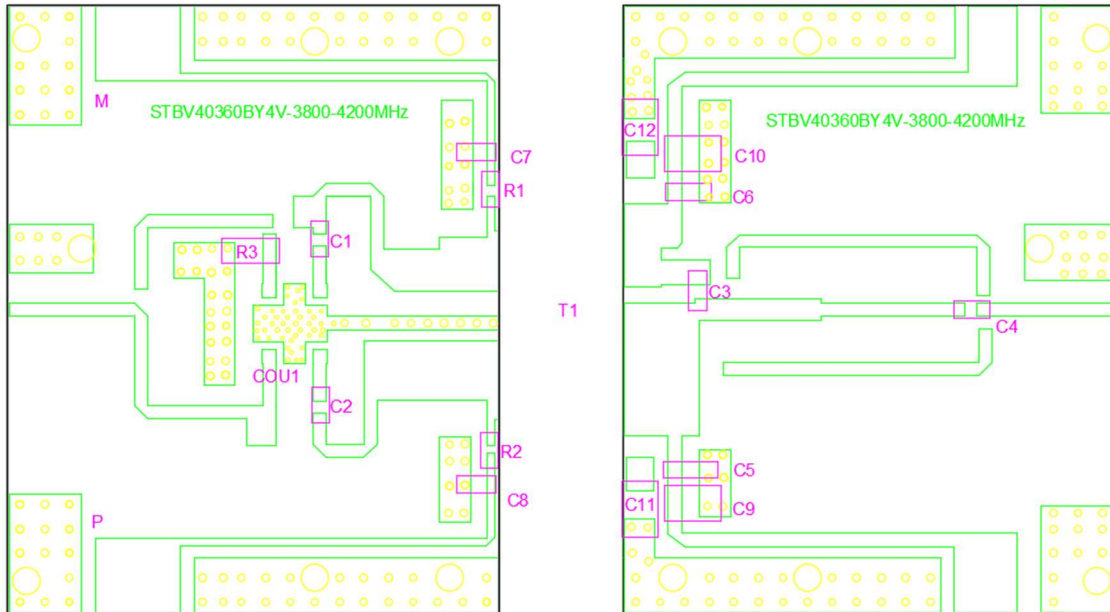
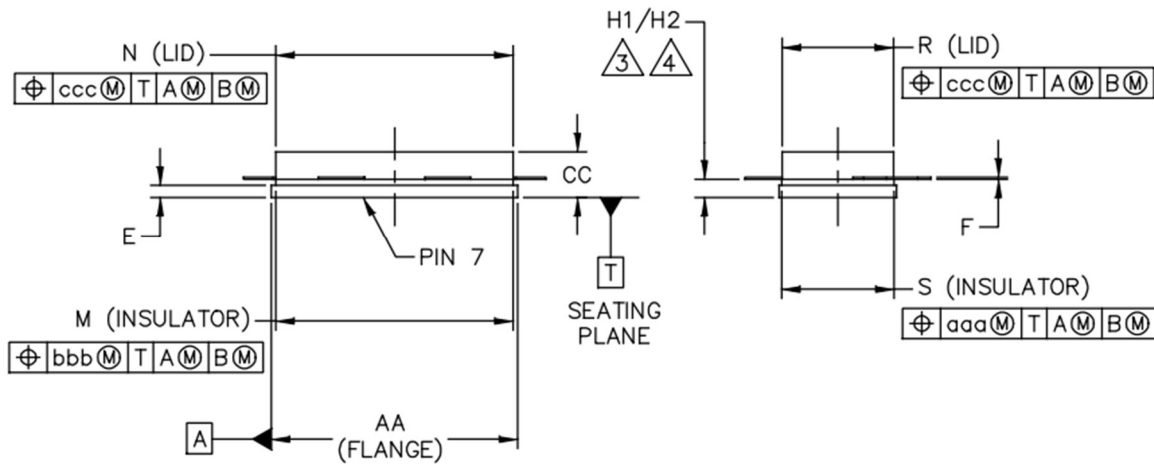
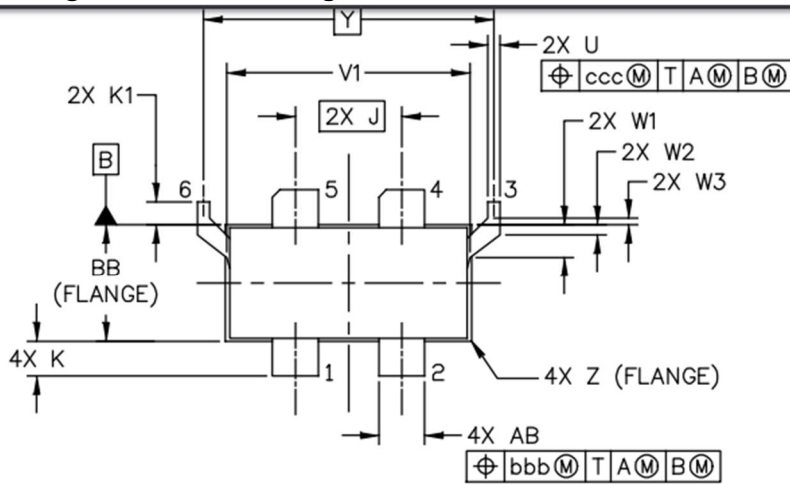


Table 4. Bill of materials of application board (PCB layout upon request, RO4350B 20mils)

Part	Quantity	Description	Part Number	Manufacture
C1,C2,C4,C6, C7,C8,C5	7	8.2pF High Q Capacitor	251SHS8R2BSE	TEMEX
C3	1	0.8pF High Q Capacitor	ATC600S0R8	ATC
C10,C11,C12,C9	4	10uF MLCC	RS80R2A106M	MARUWA
R1,R2	2	10 Ω Power Resistor	ESR03EZPF10R0	ROHM
R3	1	51 Ω Power Resistor	S1206N	RN2
COU1	1	3 dB Bridge	XC3500P-03S	ANAREN
T1	1	360W GaN Dual Transistor	STBV40360BY4V	Innogrations



Earless Flanged Ceramic Package; 6 leads- BY4V



DIM	INCH		MILLIMETER		DIM	INCH		MILLIMETER	
	MIN	MAX	MIN	MAX		MIN	MAX	MIN	MAX
AA	.805	.815	20.45	20.70	R	.365	.375	9.27	9.53
BB	.380	.390	9.65	9.91	S	.365	.375	9.27	9.53
CC	.125	.170	3.18	4.32	U	.035	.045	0.89	1.14
E	.035	.045	0.89	1.14	V1	.795	.805	20.19	20.45
F	.004	.007	0.10	0.18	W1	.0975	.1175	2.48	2.98
H1	.057	.067	1.45	1.70	W2	.0225	.0425	0.57	1.08
H2	.054	.070	1.37	1.78	W3	.0125	.0325	0.32	0.83
J	.350 BSC		8.89 BSC		Y	.956 BSC		24.28 BSC	
K	.0995	.1295	2.53	3.29	Z	R.000	R.040	R0.00	R1.02
K1	.070	.090	1.78	2.29	AB	.145	.155	3.68	3.94
M	.774	.786	19.66	19.96	aaa	.005		0.13	
N	.772	.788	19.61	20.02	bbb	.010		0.25	
					ccc	.015		0.38	



Revision history

Table 4. Document revision history

Date	Revision	Datasheet Status
2022/2/26	V1.0	Preliminary Datasheet Creation
2022/4/18	V1.1	Modify the lower frequency limits to 3.6GHz
2022/11/14	V1.2	Modify the upper frequency limits to 4.2GHz, and add 20mils PCB result

Application data based on LWH-22-06/16/17

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