Document Number: GTAH25100C6
Preliminary Datasheet V1.0

GaN HEMT 28V, 100W, General purpose RF Power Transistor Description

The GTAH25100C6 is a 75W GaN HEMT, designed for multiple applications, up to 2.5GHz.

The transistor is available in a highly cost effective 10*6mm, surface mount, QFN package with 100% DC production test to ensure the quality and consistency.

It can be used in Pulse and any other modulation modes at back off conditions

There is no guarantee of performance when this part is used in applications designed Outside of these frequencies.

Typical Class AB RF Performance with device soldered through high density and plated grounding vias
 Vds = 28V, Idq = 100mA,Vgs=-2.6V

•							
Freq	P1dB	P1dB	P1dB	P1dB	P3dB	P3dB	P3dB
(MHz)	(dBm)	(W)	Eff(%)	Gain(dB)	(dBm)	(W)	Eff(%)
2400	49.42	87.4	59.9	16.16	50.77	119.5	68.4
2450	49.01	79.6	59.5	16.03	50.45	110.9	68.1
2500	48.58	72.1	59.2	15.65	50.11	102.5	67.6

Applications

- S band power amplifier
- L band power amplifier
- 4G/5G Power amplifier

Important Note: Proper Biasing Sequence for GaN HEMT Transistors

Turning the device ON

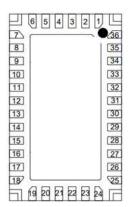
- 1. Set VGS to the pinch--off (VP) voltage, typically -5 V
- 2. Turn on VDS to nominal supply voltage
- 3. Increase VGS until IDS current is attained
- 4. Apply RF input power to desired level

Turning the device OFF

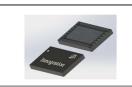
- 1. Turn RF power off
- 2. Reduce VGS down to VP, typically -5 V
- 3. Reduce VDS down to 0 V
- 4. Turn off VGS

Figure 1: Pin Connection definition

Transparent top view (Backside grounding for source)



Pin No.	Symbol	Description
8,9,10,11,14,15,16,17	RF IN/Vgs	RF Input, Vgs bias
26,27,28,29,32,33,34,35	RF OUT/VDD	RFOutput, Drain bias





Document Number: GTAH25100C6 Preliminary Datasheet V1.0

		DC/RF Ground. Must be soldered directly to heatsink or copper coin for
Rest Pins and Package Base	GND	CW application.

Table 1. Maximum Ratings

Rating	Symbol	Value	Unit
DrainSource Voltage	V _{DSS}	+150	Vdc
GateSource Voltage	V_{GS}	-8 to +0.5	Vdc
Operating Voltage	V_{DD}	36	Vdc
Maximum gate current	lgs	24.5	mA
Storage Temperature Range	Tstg	-65 to +150	°C
Case Operating Temperature	Tc	+150	°C
Operating Junction Temperature	TJ	+225	°C

Table 2. Thermal Characteristics

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Case by FEA	Rejc	1.5	°C /W
T _C = 85°C, at Pout=100W Pulsed CW	, K⊕JC	1.5	C /VV

Table 3. Electrical Characteristics (TA = 25℃ unless otherwise noted)

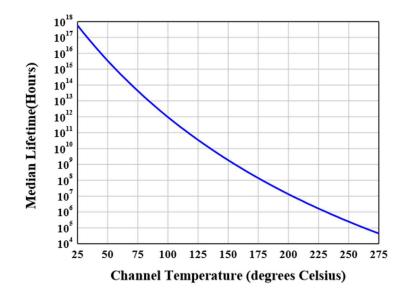
DC Characteristics (main path, measured on wafer prior to packaging)

Characteristic	Conditions	Symbol	Min	Тур	Max	Unit
Drain-Source Breakdown Voltage VGS=-8V; IDS=24.5mA		V _{DSS}		200		V
Gate Threshold Voltage VDS =10V, ID = 24.5n		$V_{GS(th)}$	-4		-2	V
Gate Quiescent Voltage	VDS =28V, IDS=100mA, Measured in Functional Test	$V_{GS(Q)}$		-2.6		V

Ruggedness Characteristics

Characteristic	Conditions	Symbol	Min	Тур	Max	Unit
Load mismatch capability	2.5GHz, Pout=100W Pulsed CW					
	All phase,	VSWR		10:1		
	No device damages					

Figure 2: Median Lifetime vs. Channel Temperature





Typical performance

Figure 3: Efficiency and power gain as function of Pout

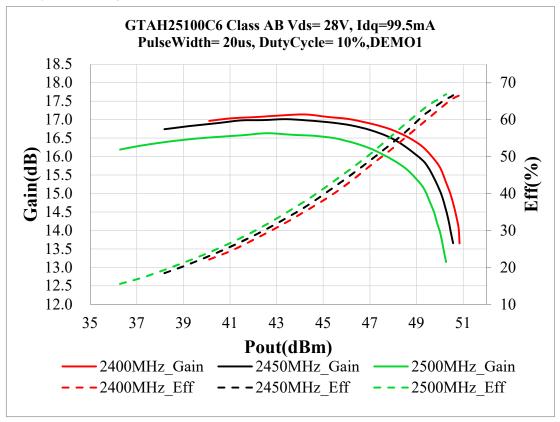
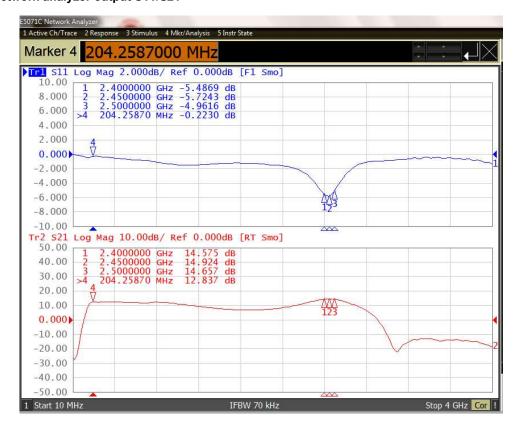


Figure 5: Network analyzer output S11/S21



Document Number: GTAH25100C6 Preliminary Datasheet V1.0

Figure 5: Picture of application board

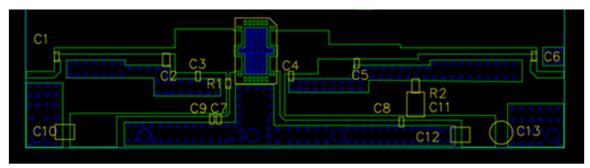
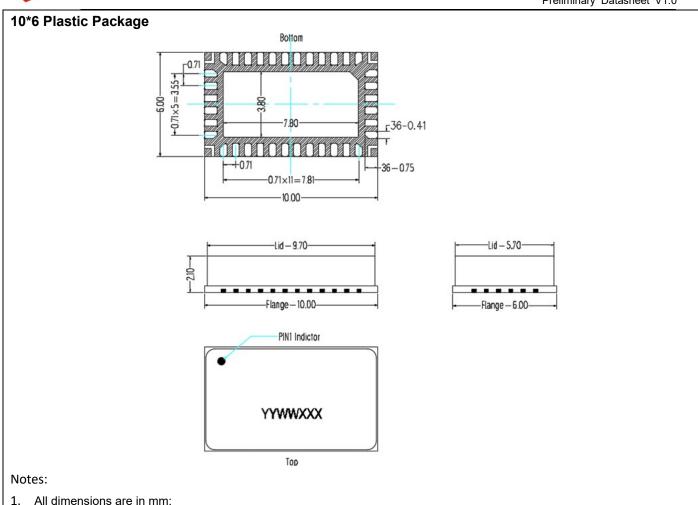


Table 4. Bill of materials of application board (PCB layout upon request, RO4350B 20mils)

	<u> </u>	<u>'</u>
Component	Value	Quantity
U1	GTAH25100C6	1
C1	5.1pF	1
C6、C7、C8	12pF	3
C9	10uF/16V	1
C10、C11、C12	10uF/63V	3
R1、R2	10 Ω	2
C13	470uF/63V	1
C2	0.8pF	1
C3	2pF	1
C4	2.7pF	1
C5	0.5pF	1



Document Number: GTAH25100C6 Preliminary Datasheet V1.0



- All dimensions are in mm;
- The tolerances unless specified are ±0.2mm.

Revision history

Table 4. Document revision history

Date Revision 2023/11/9 V1.0		Datasheet Status
		Preliminary Datasheet Creation

Application data based on: ZYX-23-11

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