

Product Features

2.7-3.1GHz:>750W, pulsed CW

>55% Drain Efficiency@50V

50ohm in and out, screw down

Device used: STBV35700BY2

Applications

5G Power amplifier

S band communication

ISM

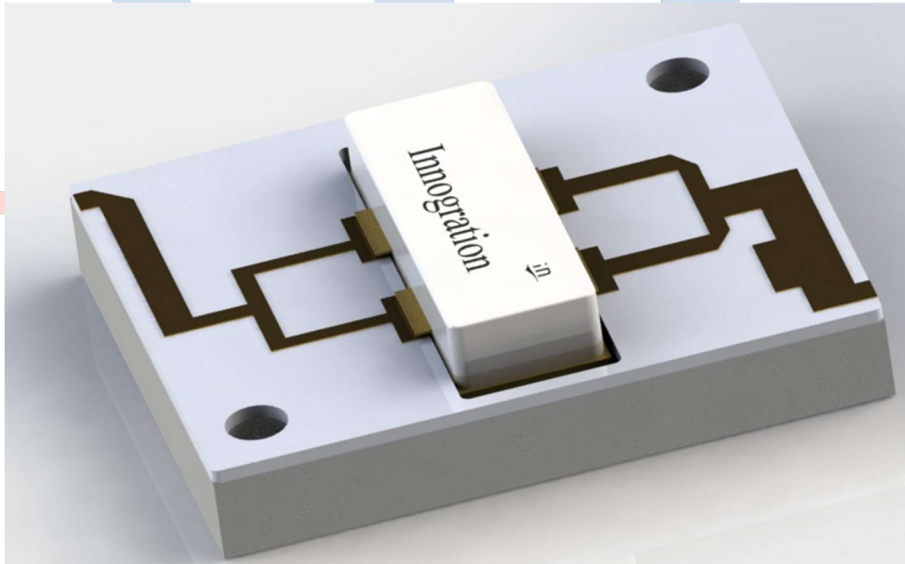
Commercial pulsed CW Power amplifier

Description

The SMPA2731-750V is designed for 5G communication, test and measurement and other ISM applications at 2700-3100MHz. This Amplifier pallet is suitable for use in linear and saturated applications. Featured by 50ohm fully matched at input and output, drop-in placement by screwing it down and 100% RF test, it enables easier power combination to reach higher power with high production yield as part of customer's power amplifier system.

This standard pallet is with typical size 50*90mm, but can be shrunk to much smaller size.

Pallet concept demostration purpose only, Not exactly the design itself





Electrical Specifications @VCC=50V, T=25°C, 50Ωsystem

PARAMETER	UNIT	MIN	TYP	MAX	SYMBOL
Operating Frequency	MHz	2700	-	3100	fo
Operating Bandwidth	MHz	400		-	OBW
Pulse CW Output Saturated Power	W	750		-	Psat
Power Gain	dB	9	10	-	G _P
Gain Flatness	dB	-	-	±0.5	G _F
Input Return Loss	dB	-3	-	-10	S ₁₁
Operating Voltage	V	-	50	55	V _{DS}
Quiescent Current	mA	-	100	-	I _{DQ}
Efficiency@Psat	%	55		-	Eff

Environmental Characteristics

PARAMETER	UNIT	MIN	TYP	MAX	SYMBOL
Operating Case Temperature	°C	-40	-	60	Ta
Storage Temperature	°C	-40		100	Tstg
Relative humidity w/o condensation	%	-	-	95	RH

Mechanical Specifications

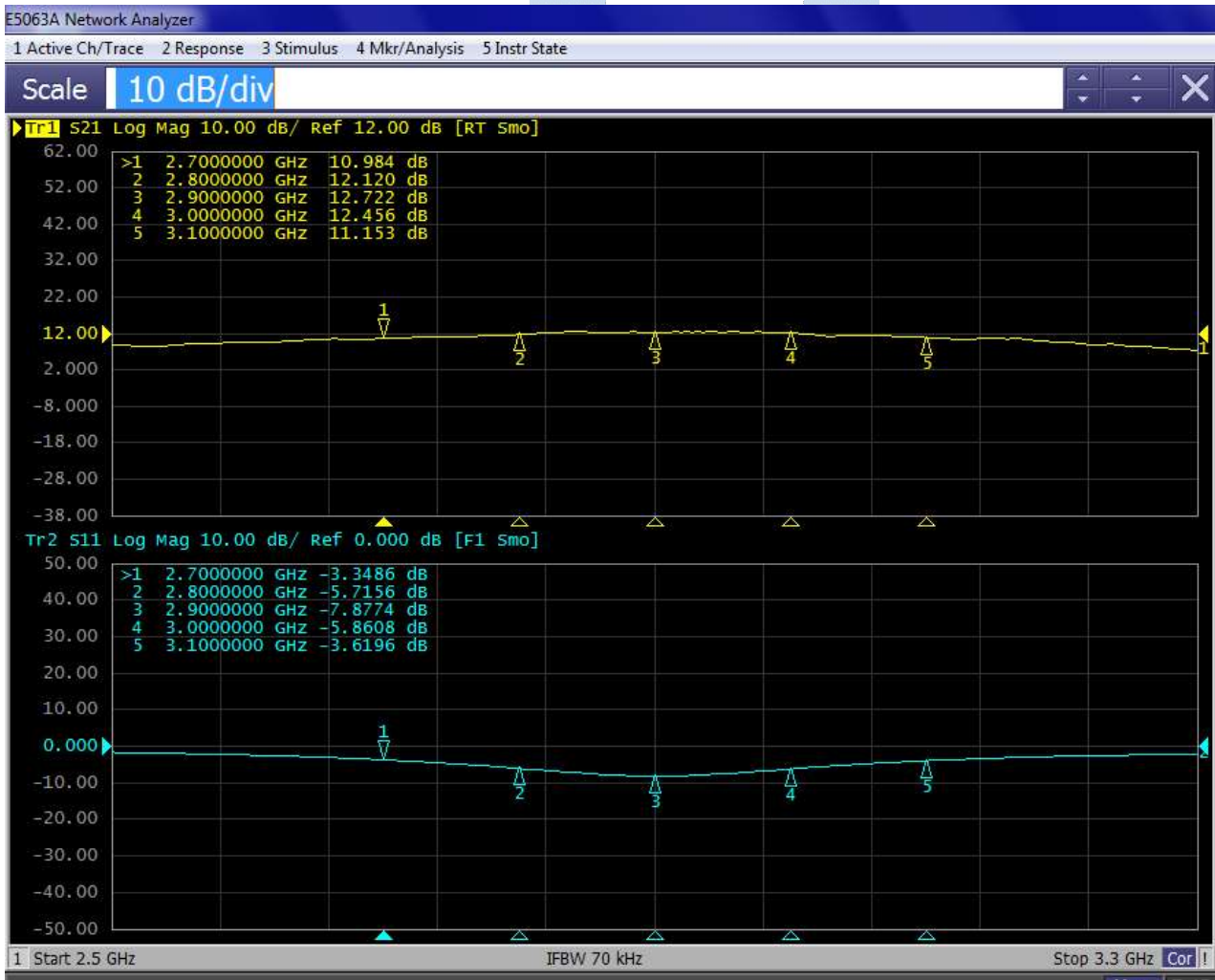
PARAMETER	UNIT	VALUE
Dimensions(L × W × H)	mm	50×90×4
RF Input Connector	-	N/A
RF Output Connector	-	N/A
Cooling	-	External Heat-sink

Typical performance

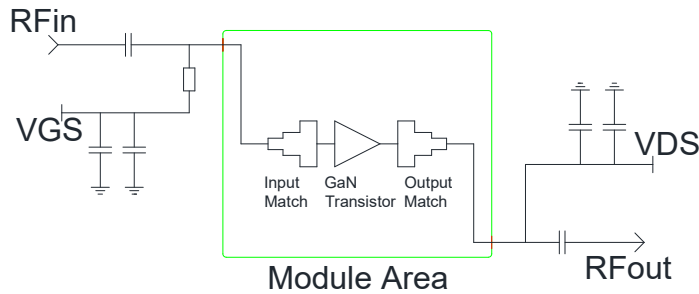
- Pulsed CW performance:

STBV35700BY2 v0 Vds=50V,Vgs=-3.46V,Idq=100mA Pulse:100us,20%						
Freq(MHz)	Pin(dBm)	Psat(dBm)	Psat(W)	Ids(A)	Gain(dB)	Eff(%)
2700	50.24	59.72	937.6	3.18	9.5	59.1
2800	49.20	59.79	952.8	3.34	10.6	57.1
2900	48.85	59.95	988.6	3.40	11.1	58.2
3000	49.07	59.82	959.4	3.21	10.8	59.8
3100	50.46	59.61	914.1	3.00	9.2	60.9

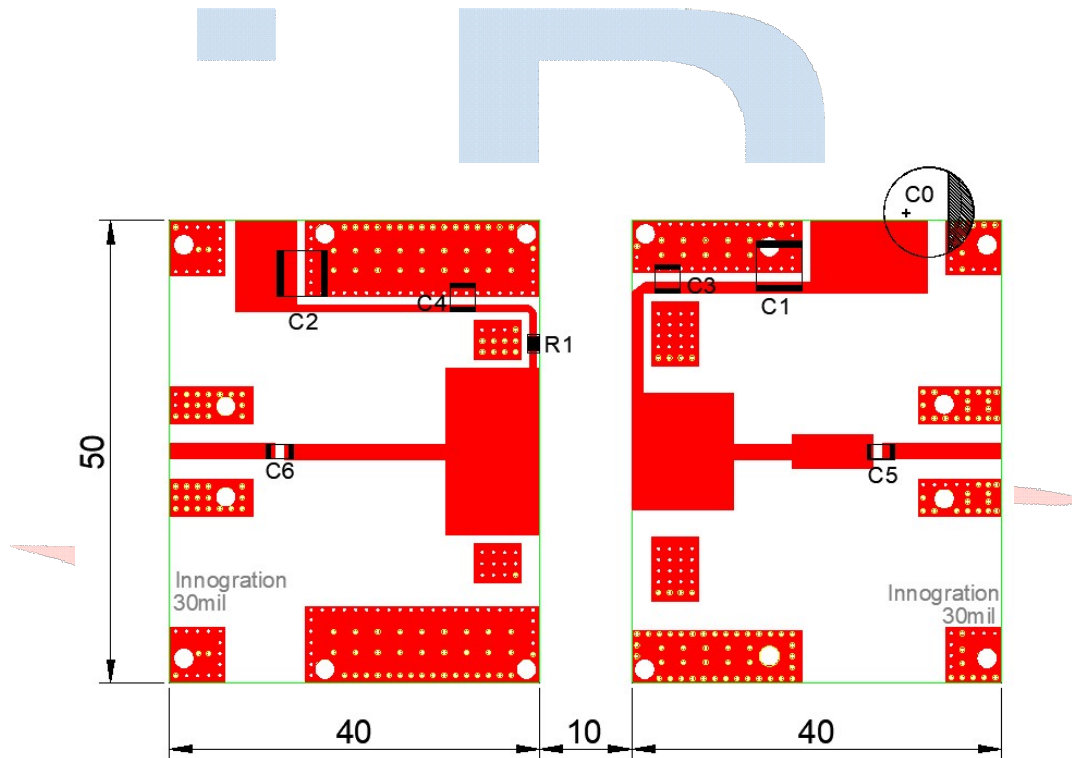
- S21/S11 from network analyzer VDS=50V VGS=-3.02V IDQ=500mA



Evaluation board Block Diagram



Evaluation board outline (DUT:STBV35700BY2)



Component	Description	Suggestion
C0	4700uF/63V	
C1,C2	10uF	5750
C3,C4	10pF	MQ101111
C5,C6	10pF	MQ301111
R1	Chip Resistor,10Ω	0805
PCB	30 Mil Rogers 4350B	



Revision History

Document revision history

Date	Revision	Datasheet Status
2023/7/8	Rev 1.0	Preliminary Datasheet

Application data based on RXT-23-29



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