



45W,28V Plastic RF LDMOS Transistor

ITGV10090C6

Description

The ITGV10090C6 is a 45-watt, highly rugged, LDMOS transistor, designed for any general applications at frequencies from HF to 0.7GHz, in 10*6mm QFN plastic package, supporting surface mounted on PCB through high density grounding vias.

It can support CW, pulsed CW and any modulation signal at saturated or linear conditions.



- Typical 370-430MHz Class AB RF Performance (On Innegration fixture with device soldered).
Vds=28V, Idq=300mA, Vgs=3.43V

FREQ (MHZ)	P1dB (dBm)	P1dB (W)	P1dB Eff(%)	P1dB Gain(dB)	P3dB (dBm)	P3dB (W)	P3dB Eff(%)
370	45.88	38.7	59.6	21.28	47.04	50.5	66.8
400	46.02	40.0	62.5	21.17	47.11	51.4	69.5
430	45.91	39.0	66.9	22.85	46.82	48.1	73.1

Features

- High Efficiency and Linear Gain Operations
- Integrated ESD Protection
- Excellent thermal stability, low HCI drift
- Large Positive and Negative Gate/Source Voltage Range for Improved Class C Operation
- Pb-free, RoHS-compliant

Suitable Applications

- P band power amplifier
- HF/VHF/UHF Power amplifier

Table 1. Maximum Ratings

Rating	Symbol	Value	Unit
Drain--Source Voltage	V _{DSS}	+110	Vdc
Gate--Source Voltage	V _{GS}	-10 to +10	Vdc
Operating Voltage	V _{DD}	+36	Vdc
Storage Temperature Range	T _{stg}	-65 to +150	°C
Case Operating Temperature	T _c	+150	°C
Operating Junction Temperature	T _j	+225	°C

Table 2. Thermal Characteristics

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Case T _c = 85°C, DC test	R _{θJC}	2	°C/W

Table 3. ESD Protection Characteristics

Test Methodology	Class
Human Body Model (per JESD22--A114)	Class 2



Table 4. Electrical Characteristics (TA = 25 °C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
DC Characteristics					
Drain-Source Voltage V _{GS} =0, I _{DS} =100uA	V _{(BR)DSS}		110		V
Zero Gate Voltage Drain Leakage Current (V _{DS} = 28V, V _{GS} = 0 V)	I _{DSS}	—	—	1	μA
Gate--Source Leakage Current (V _{GS} = 11 V, V _{DS} = 0 V)	I _{GSS}	—	—	1	μA
Gate Threshold Voltage (V _{DS} = 28V, I _D = 600 μA)	V _{GS(th)}	—	2	—	V
Gate Quiescent Voltage (V _{DD} = 28V, I _D = 300mA, Measured in Functional Test)	V _{GS(Q)}	—	3.4	—	V

Load Mismatch (In Innogrations Test Fixture, 50 ohm system): V_{DD} = 28Vdc, I_{DQ} = 300 mA, f = 700 MHz

VSWR 10:1 at 45W pulse CW Output Power	No Device Degradation
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Figure 1: Pin Definition(Top View)



Pin No.	Symbol	Description
8,9,10,11,14,15,16,17	Vgs/RF In	Vgs and RF input
26,27,28,29,32,33,34,35	Vds/RF out	Vds and RF output
2,5,7,12,13,18,20,23,25,30,31,36	GND	DC/RF Ground
Others	NC	No connection
Package Base	GND	DC/RF Ground.

370-430MHz application board

Reference Circuit of Test Fixture Assembly Diagram 20mils RO4350B

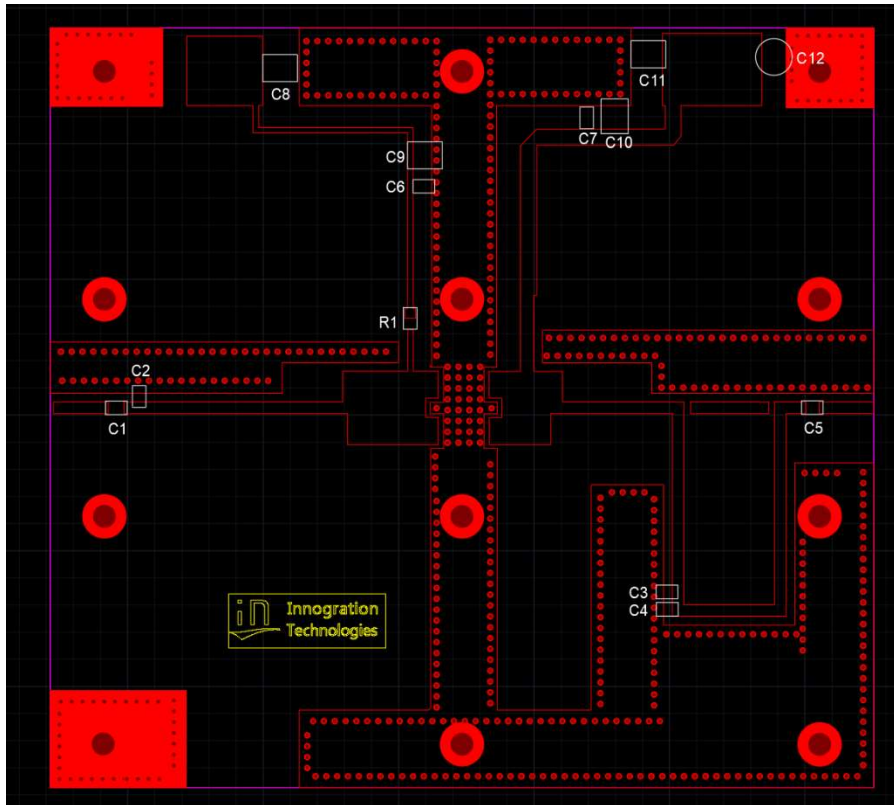


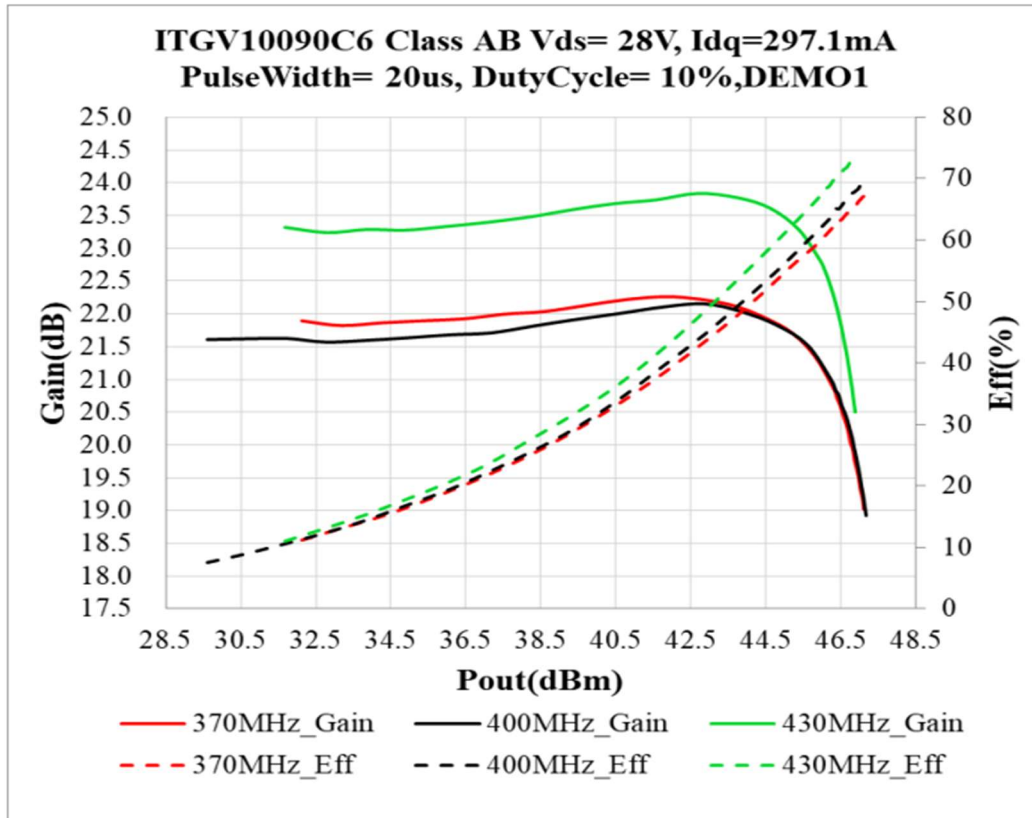
Figure 2. Test Circuit Component Layout

Table 5. Test Circuit Component Designations and Values

Component	Value	Quantity
U1	ITGV10090C6	1
C1 、 C5、 C6、 C7	33pF	4
C2	15pF	1
C3	9.2pF	1
C4	6.8pF	1
C8、 C9、 C10、 C11	10uF/63V	4
R1	10 Ω	1
C12	470uF/63V	1

TYPICAL CHARACTERISTICS

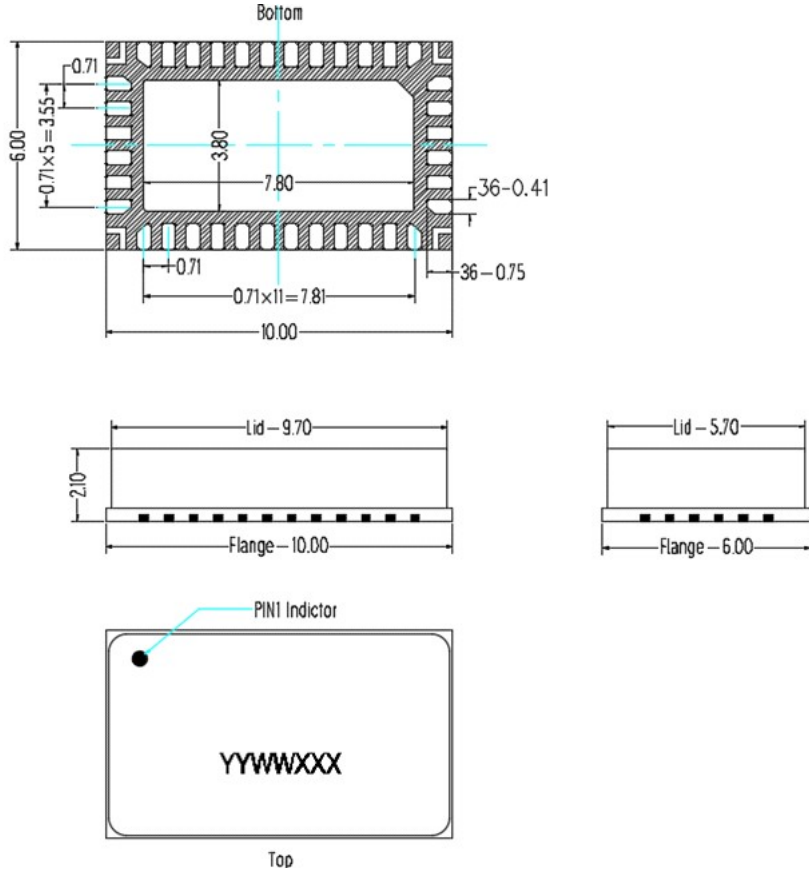
Figure 3. Power Gain and Drain Efficiency as function of Power Output





Package Dimensions

10*6 Plastic Package



Notes:

- 1. All dimensions are in mm;
- 2. The tolerances unless specified are ±0.2mm.

Revision history

Table 7. Document revision history

Date	Revision	Datasheet Status
2023/09/04	Rev 1.0	Preliminary Datasheet

Application data based on ZYX-23-09

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