Innogration

GaN Power Amp Pallet

SMPA5759-400V

Product Features

5.7-5.9GHz:>400W, Pulsed CW

Typically 48% Drain Efficiency@50V 50ohm in and out, 50*30mm, screw down Device used: STCV58400F4

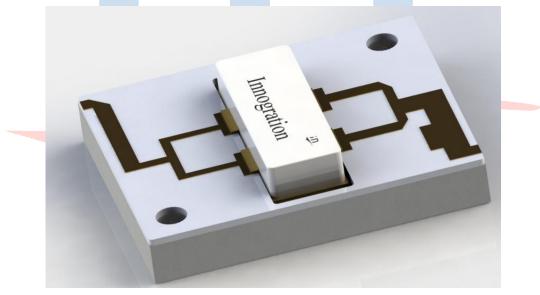
Applications

5G Power amplifier C band communication ISM Commercial pulsed CW Power amplifier

Description

The SMPA5759-400V is designed for 5G communication, test and measurement and other ISM applications at 5700-5900MHz. This Amplifier pallet is suitable for use in linear and saturated applications. Featured by its tiny size 50*30mm, and 500hm fully matched at input and output, dropin placement by screwing it down and 100% RF test, it enables easier power combination to reach higher power with high production yield as part of customer's power amplifier system.

Pallet concept demostration purpose only, Not exactly the design itself



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Electrical Specifications @vcc=50v, T=25°C, 50Ωsystem

PARAMETER	UNIT	MIN	ТҮР	MAX	SYMBOL
Operating Frequency	MHz	5700	-	5900	fo
Operating Bandwidth	MHz		200	-	OBW
Pulse CW Output Power	W		400	-	Pout
Power Gain	dB		10	-	GP
Gain Flatness	dB	-	-	±0.5	G _F
Input Return Loss	dB	-	10		S ₁₁
Operating Voltage	v	-	50	60	V _{DS}
Quiescent Current	mA	-	100	-	I _{DQ}
Efficiency@Psat	%	45	50	-	Eff

Environmental Characteristics

PARAMETER	UNIT	MIN	ТҮР	MAX	SYMBOL
Operating Case Temperature	°C	-40	-	60	Та
Storage Temperature	°C	-40		100	Tstg
Relative humidity w/o condensation	%	-	-	95	RH

Mechanical Specifications

PARAMETER	UNIT	VALUE		
Dimensions(L × W × H)	mm	50×30×10		
Weight	g	300		
RF Input Connector	-	N/A		
RF Output Connector	-	N/A		
Cooling	-	External Heat-sink		

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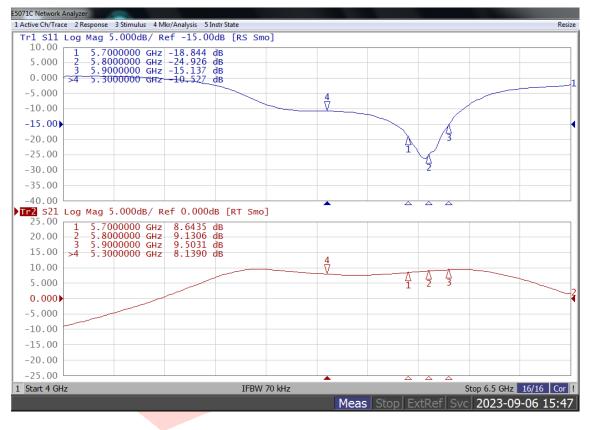


Typical performance

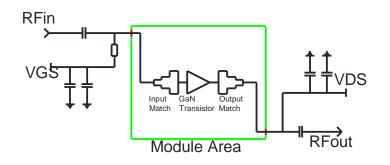
• Pulsed CW performance: VDS=50V VGS=-3.3V IDQ=200mA, Pulse: 20uS width, 10%;

Freq	P1dB	P1dB	P1dB	P1dB	P3dB	P3dB	P3dB
(MHz)	(dBm)	(W)	Eff(%)	Gain(dB)	(dBm)	(W)	Eff(%)
5700	54.55	284.9	41.1	12.27	56.45	441.3	46.9
5800	54.77	299.9	43.2	13	56.67	464.3	49.2
5900	54.26	266.6	44.0	12.39	56.3	426.4	50.8

• S21/S11 from network analyzer VDS=50V VGS=-3.02V IDQ=500mA



Evaluation board Block Diagram



Evaluation board outline (DUT:STCV58400F4) PCB RO4350B 20mils

Designator	Comment	Footprint	Quantity
C1, C2, C3, C4	3.9 pF/250V	0805	4
C5, C6	10uF/100V	1210	2
C7	100uF/63V		1
R1, R2	10 Ω	0603	2

Revision History

Document revision history

Date	Revisio	Datasheet Status
2023/9/6	Rev 1.0	0 Preliminary Datasheet

Application data based on LSM-23-29

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