



GaN HEMT 50V, 800W, 3.4-3.8GHz Full band RF Power Transistor

Description

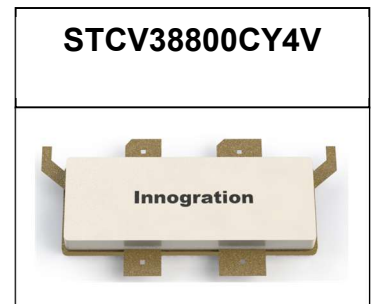
The STCV38800CY4V is a 800-watt, internally matched GaN HEMT, designed for 5G cellular applications with frequencies from 3.4-3.8GHz, **enabled by wide band VBW capability to support IBW typically 200MHz.**

It can be configured as asymmetrical Doherty for 5G application, delivering 100W average power, according to normal 9dB back off.

There is no guarantee of performance when this part is used in applications designed Outside of these frequencies.

- Typical Doherty Pulsed CW and 1C W--CDMA Characterization Performance:

$V_{DD} = 50\text{ Vdc}$, $I_{DQA} = 300\text{ mA}$, $V_{GSB} = -5.9\text{ Vdc}$, 1C WCDMA; Signal PAR = 10 dB @ 0.01% Probability on CCDF.



Freq (GHz)	Pulse CW Signal ⁽¹⁾				$P_{avg}=50.0\text{dBm}$ WCDMA Signal ⁽²⁾		
	P1 (dBm)	P1 (W)	P3.5 (dBm)	P3.5 (W)	Gp (dB)	η_D (%)	ACPR _{5M} (dBc)
3.40	56.95	495	59.05	803	12.11	39.32	-26.31
3.50	56.60	460	59.10	813	11.49	39.95	-29.43
3.60	57.48	560	59.15	822	11.15	41.05	-34.44
3.70	57.23	529	59.18	828	11.01	41.07	-32.37
3.80	57.73	593	59.03	800	10.80	39.46	-32.40

Recommended driver: STBV38130C9(1 stage Doherty discrete) or SMBV3438-201 (2 stage Doherty MCM)

Applications

- Asymmetrical Doherty amplifier within N78 5G band
- S band power amplifier

Important Note: Proper Biasing Sequence for GaN HEMT Transistors

Turning the device ON

1. Set VGS to the pinch—off (VP) voltage, typically -5 V
2. Turn on VDS to nominal supply voltage
3. Increase VGS until IDS current is attained
4. Apply RF input power to desired level

Turning the device OFF

1. Turn RF power off
2. Reduce VGS down to VP, typically -5 V
3. Reduce VDS down to 0 V
4. Turn off VGS

Figure 1: Pin Connection definition

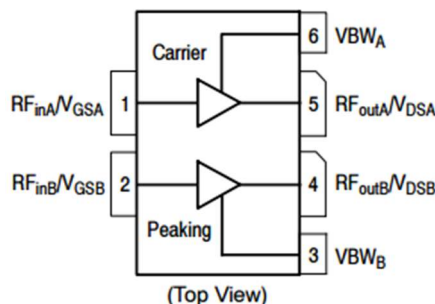




Table 1. Maximum Ratings

Rating	Symbol	Value	Unit
Drain—Source Voltage	V_{DSS}	+200	Vdc
Gate—Source Voltage	V_{GS}	-8 to +0.5	Vdc
Operating Voltage	V_{DD}	55	Vdc
Maximum gate current	I_{gs}	116	mA
Storage Temperature Range	T_{stg}	-65 to +150	°C
Case Operating Temperature	T_c	+150	°C
Operating Junction Temperature	T_J	+225	°C

Table 2. Thermal Characteristics

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Case by FEA $T_c=85^\circ\text{C}$, $P_{out}=100\text{W}$, 3.6GHz Doherty application board	$R_{\theta JC}$	0.7	°C /W

Table 3. Electrical Characteristics (TA = 25°C unless otherwise noted)

DC Characteristics (main path, measured on wafer prior to packaging)

Characteristic	Conditions	Symbol	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$V_{GS}=-8\text{V}$; $I_{DS}=56\text{mA}$	V_{DSS}		200		V
Gate Threshold Voltage	$V_{DS}=10\text{V}$, $I_D=56\text{mA}$	$V_{GS(th)}$	-4		-2	V
Gate Quiescent Voltage	$V_{DS}=50\text{V}$, $I_{DS}=300\text{mA}$, Measured in Functional Test	$V_{GS(Q)}$		-3.2		V

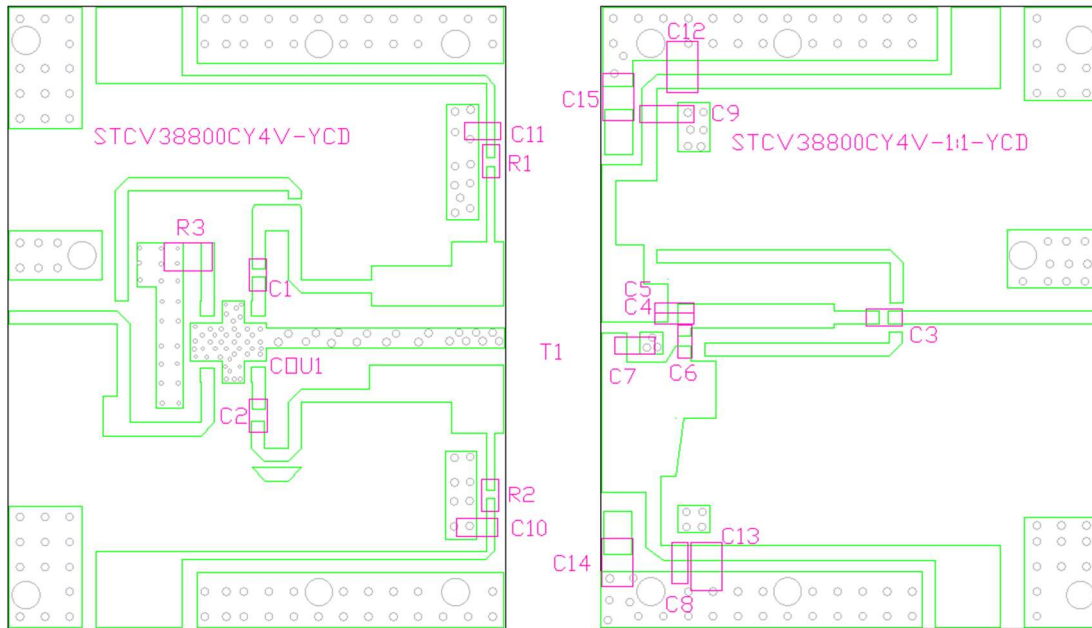
DC Characteristics (peak path, measured on wafer prior to packaging)

Characteristic	Conditions	Symbol	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$V_{GS}=-8\text{V}$; $I_{DS}601\text{mA}$	V_{DSS}		200		V
Gate Threshold Voltage	$V_{DS}=10\text{V}$, $I_D=60\text{mA}$	$V_{GS(th)}$	-4		-2	V
Gate Quiescent Voltage	$V_{DS}=50\text{V}$, $I_{DS}=400\text{mA}$, Measured in Functional Test	$V_{GS(Q)}$		-3.2		V

Ruggedness Characteristics

Characteristic	Conditions	Symbol	Min	Typ	Max	Unit
Load mismatch capability	3.6GHz, $P_{out}=100\text{W}$ WCDMA 1 Carrier in Doherty circuit All phase, No device damages	VSWR		10:1		

Figure 3: Picture of application board Doherty circuit for 3.4-3.8GHz



Part	Quantity	Description	Part Number	Manufacture
C1,C2,C3,C8, C9,C10,C11	7	8.2pF High Q Capacitor	251SHS8R2BSE	TEMEX
C4,C5,C7	3	0.7pF High Q Capacitor	251SHS0R7BSE	TEMEX
C6	1	1.3pF High Q Capacitor	ATC600F1R3	ATC
C12,C13,C14,C15	4	10uF MLCC	RS80R2A106M	MARUWA
R1,R2	2	10 Ω Power Resistor	ESR03EZPF100	ROHM
R3	1	51 Ω Power Resistor	RFR50-20CT0421B	YT
COU1	1	3 dB Bridge	XC3500P-03S	ANAREN
T1	1	800W GaN Dual Transistor	STCV38800CY4V	Innegration



Figure 4: Efficiency and power gain as function of Pout

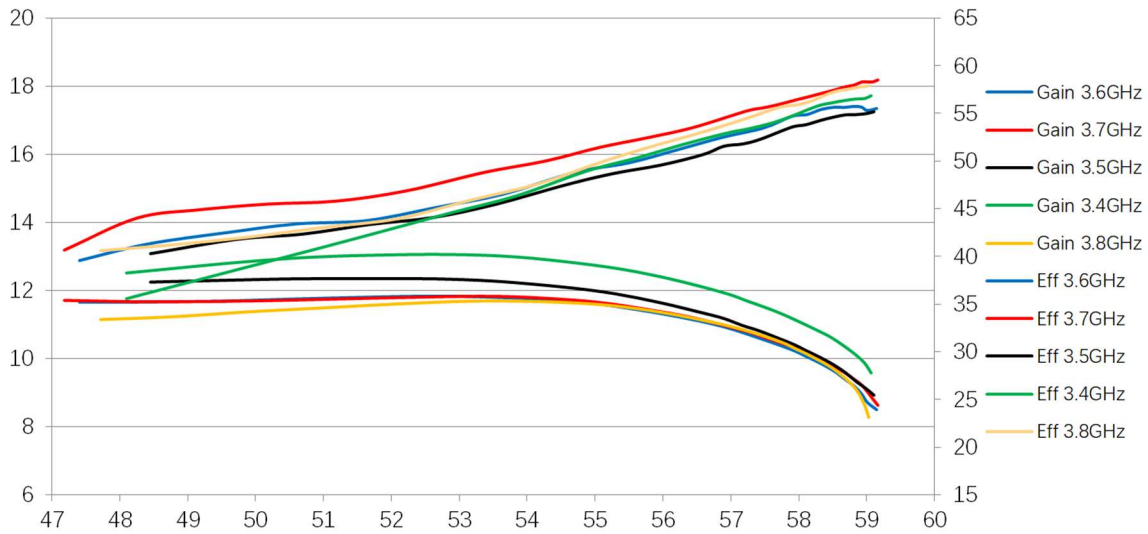
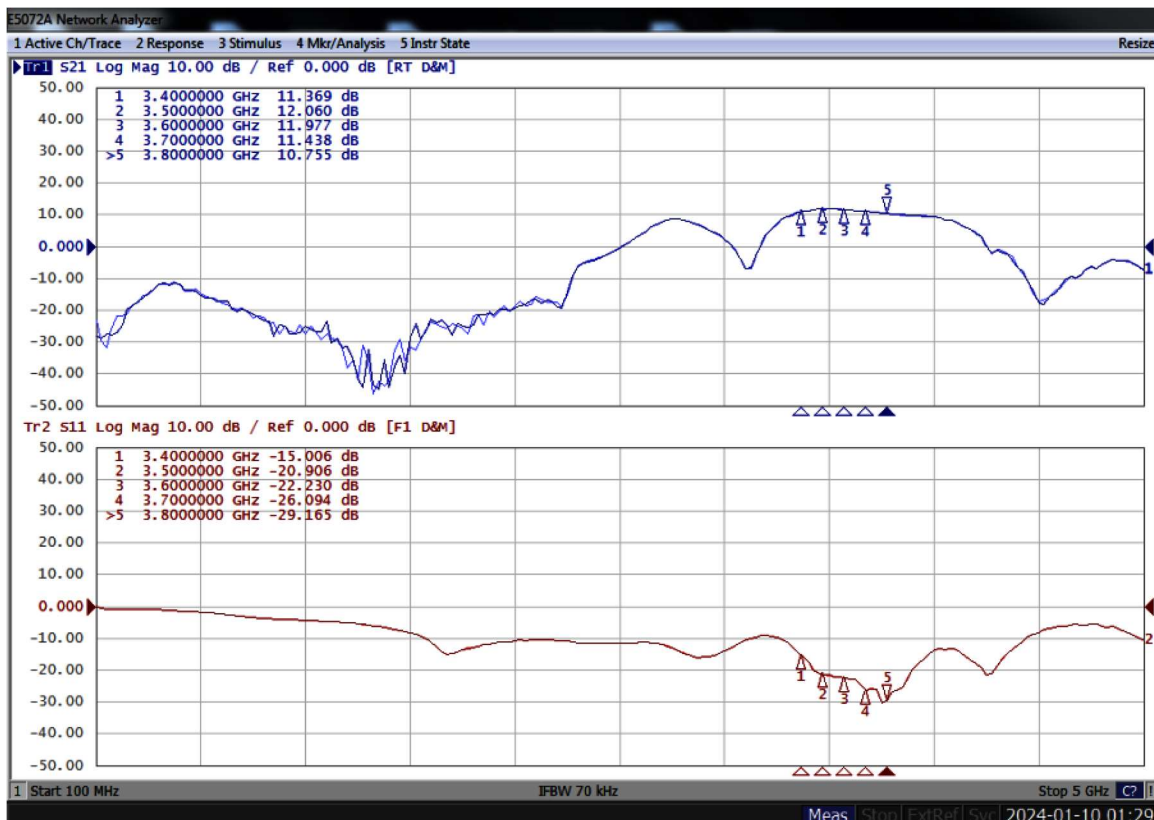
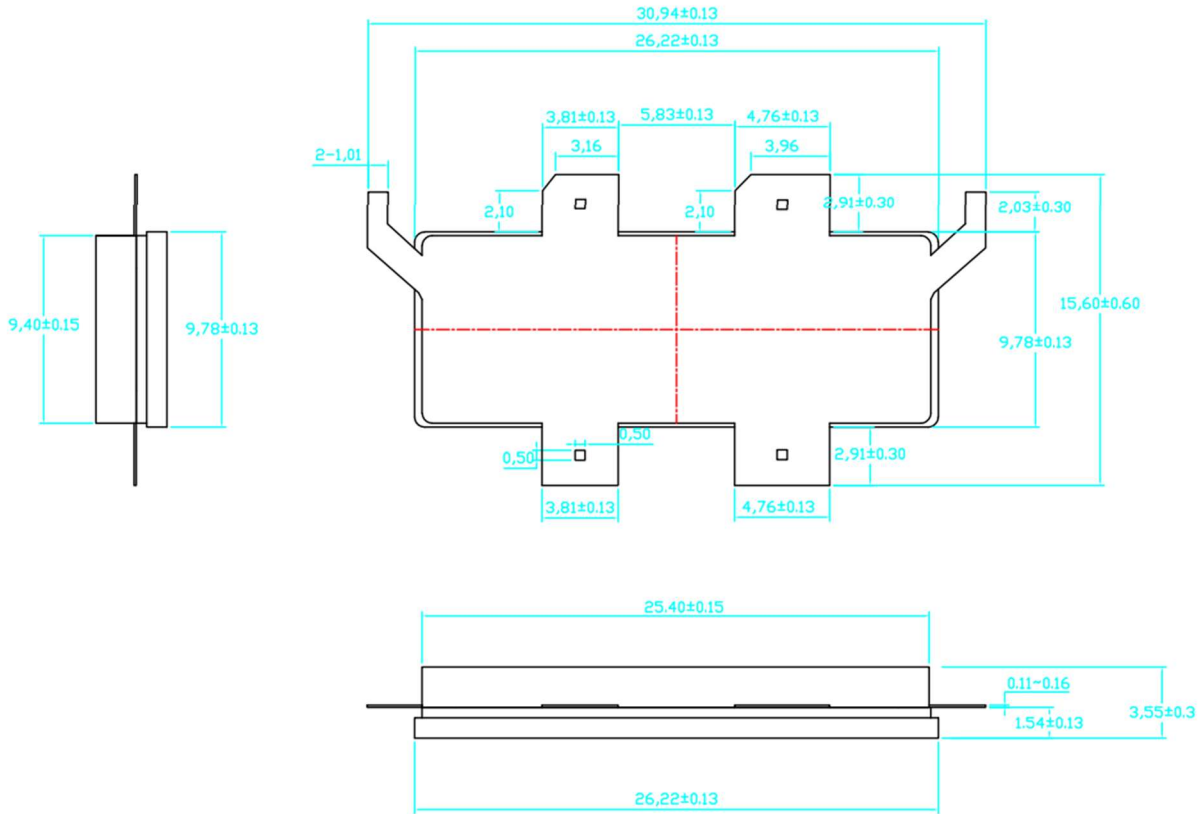


Figure 5: Network analyzer output, S11 and S21





Earless Flanged Ceramic Package; 6 leads- CY4V



Revision history

Table 4. Document revision history

Date	Revision	Datasheet Status
2023/1/10	V1.0	Preliminary Datasheet Creation

Application data based on LWH-24-02

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