## GaN HEMT 50V, 200W, 5.8GHz RF Power Transistor

## Description

The STCV58200F4C is a single ended 200watt, GaN HEMT, ideal for ISM applications at 5.8GHz. It can support CW, pulse and linear applications.

There is no guarantee of performance when this part is used outside of stated frequencies.

- Typical pulse CW performance across the band with device soldered
- VDD = 50 Vdc,Idq=100mA Tc=25°C, air cooling

CW:

Freq (MHz)	Pin(dBm)	Psat(dBm)	Psat(W)	lds(A)	Gain(dB)	Eff(%)
5800	42.7	53.36	216.77	7.82	10.66	55.44

Recommended driver: STAV58030J2

## Applications

- C band Class AB power amplifier
- 5.8GHz RF Energy

## Important Note: Proper Biasing Sequence for GaN HEMT Transistors

### Turning the device ON

- Turning the device OFF
- 1. Set VGS to the pinch--off (VP) voltage, typically –5 V  $\,$
- 2. Turn on VDS to nominal supply voltage
- 3. Increase VGS until IDS current is attained
- 4. Apply RF input power to desired level

- 1. Turn RF power off
- 2. Reduce VGS down to VP, typically –5 V
- 3. Reduce VDS down to 0 V
- 4. Turn off VGS

### Table 1. Maximum Ratings

Rating	Symbol	Value	Unit
DrainSource Voltage	V <sub>DSS</sub>	+200	Vdc
GateSource Voltage	V <sub>GS</sub>	-8 to +0.5	Vdc
Operating Voltage	V <sub>DD</sub>	55	Vdc
Maximum gate current	lgs	25.2	mA
Storage Temperature Range	Tstg	-65 to +150	°C
Case Operating Temperature	Tc	+150	°C
Operating Junction Temperature	TJ	+225	°C

### **Table 2. Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Case by FEA	<b>D</b> alo	0.75	°C /W
T <sub>c</sub> = 25°C, at Pout=200W at 5.8GHz	Rejc	0.75	C /W

#### Table 3. Electrical Characteristics (TA = 25°C unless otherwise noted)

#### **DC Characteristics**

Characteristic	Conditions	Symbol	Min	Тур	Max	Unit
Drain-Source Breakdown Voltage	VGS=-8V; IDS=25.2mA	V <sub>DSS</sub>		200		V



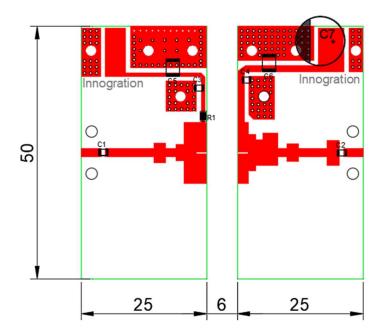
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Gate Threshold Voltage	VDS =10V, ID = 25.2mA	V <sub>GS(th)</sub>	-4	-	-2	V
Gate Quiescent Voltage	VDS =50V, IDS=100mA, Measured in Functional Test	V <sub>GS(Q)</sub>		3.35		V
Ruggedness Characteristics						

Characteristic	Conditions	Symbol	Min	Тур	Max	Unit
Load mismatch capability	5.8GHz, Pout=200W pulse CW					
	All phase,	VSWR		10:1		
	No device damages					

## **Reference Circuit of Test Fixture Assembly Diagram**

PCB materials: Ro4350B , DXF file upon request



Component	Description	Suggestion		
C7	470uF/63V			
C5,C6	10uF	10uF/100V		
C1, C2, C3 C4	3.9pF	MQ300805		
R1	Chip Resistor,10Ω	0805		
РСВ	30mil Rogers 4350B			

### Figure 3: Efficiency and power gain as function of Pout

(VDD = 50 Vdc, IDQ = 100mA, Pulse width=20us, duty cycle=10%, 5.8GHz)

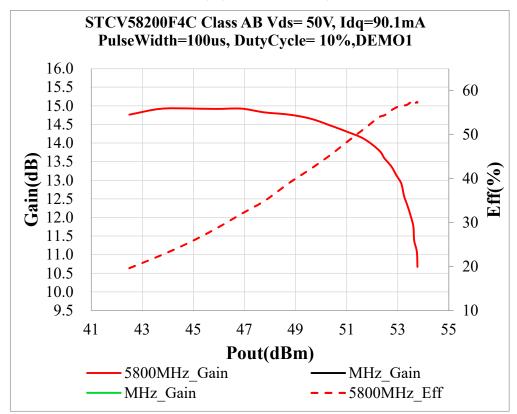
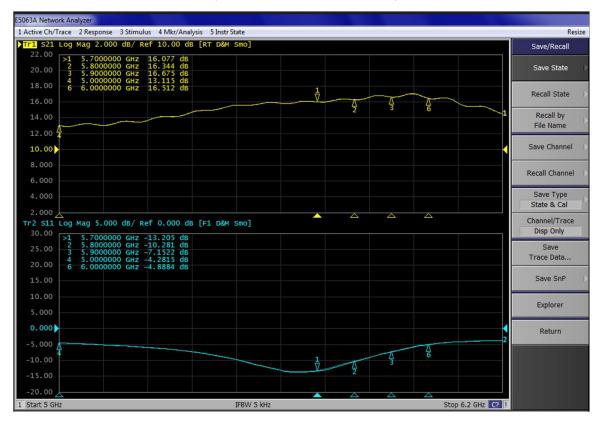
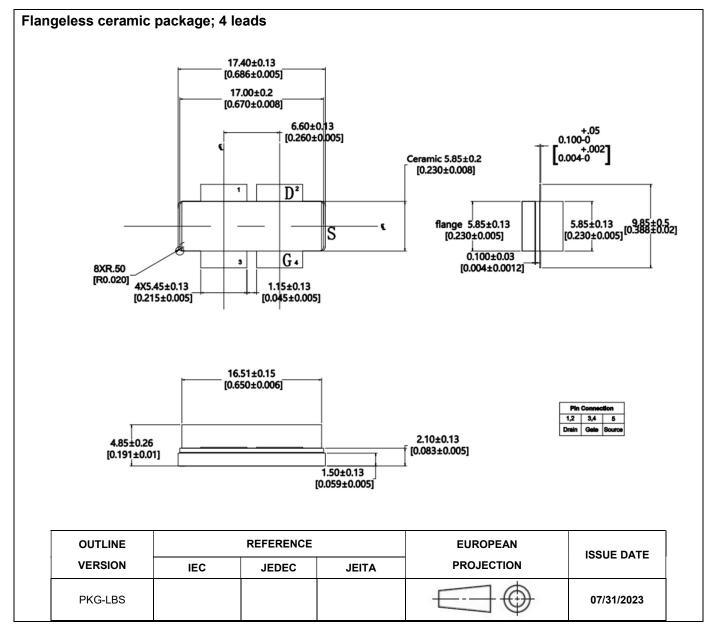


Figure 4: S11/S21 output from Network analyser (VDS= 50V, IDQ=500 mA Vgs =-3.2V)



## **Package Outline**



### **Revision history**

#### Table 4. Document revision history

Date	Revision	Datasheet Status
2024/4/25	V1.0	Preliminary Datasheet Creation

Application data based on: YHG-24-06

### Notice

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