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Document Number: GTAH58008C6 Preliminary Datasheet V1.0

GaN HEMT 28V, 5.8GHz 8W, RF Power Transistor

Description

The GTAH58008C6 is a 8W GaN HEMT, designed for ISM/RF Energy application around 5.8GHz The transistor is available in a highly cost effective 10*6mm, surface mount, QFN package with100% production test to ensure the quality and consistency.

It can be used in CW, Pulse and any other modulation modes.

Typical Class AB RF Performance with device soldered through high density and plated grounding vias CW, Vds=28V, Idq=20mA

Freq	P1dB	P1dB	P1dB	P1dB	P3dB	P3dB	P3dB
(MHz)	(dBm)	(W)	Eff(%)	Gain(dB)	(dBm)	(W)	Eff(%)
5100	39.64	9.20	51.86	14.50	40.92	12.36	56.86
5200	39.57	9.05	53.55	15.11	40.76	11.91	57.61
5300	39.49	8.89	54.52	15.45	40.48	11.17	56.91
5400	39.24	8.40	54.66	15.84	40.12	10.27	55.38
5500	39.07	8.07	54.54	16.03	40.04	10.10	56.57
5600	39.02	7.97	56.11	16.33	40.20	10.47	61.18
5700	38.84	7.66	57.85	16.57	39.97	9.94	62.05
5800	38.57	7.20	57.05	16.14	39.67	9.26	60.35
5900	38.51	7.09	54.14	14.95	39.49	8.89	56.67

Applications

- C band power amplifier
- ISM/RF Energy power amplifier

Important Note: Proper Biasing Sequence for GaN HEMT Transistors

Turning the device ON

- 1. Set VGS to the pinch--off (VP) voltage, typically -5 V
- 2. Turn on VDS to nominal supply voltage
- 3. Increase VGS until IDS current is attained
- 4. Apply RF input power to desired level

Figure 1: Pin Connection definition

Turning the device OFF

- 1. Turn RF power off
- 2. Reduce VGS down to VP, typically -5 V
- 3. Reduce VDS down to 0 V
- 4. Turn off VGS

Transparent top view (Backside grounding for source)

	6 5 4 3 2	1
D		0/36
8		35
9		34
10		33
11		32
12		31
13		30
14		29
15		28
16		27
17		26
18	L	25
51	19 20 21 22 23	24 6

Pin No.	Symbol	Description		
8,9,10,11	RF IN/Vgs	RF Input, Vgs bias		
32,33,34,35 RF OUT/VDD		RFOutput, Drain bias		
		DC/RF Ground. Must be soldered directly to heatsink or copper coin for		
Rest Pins and Package Base	GND	CW application.		





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Table 1. Maximum Ratings

Rating	Symbol	Value	Unit
DrainSource Voltage	V _{DSS}	+150	Vdc
GateSource Voltage	V _{GS}	-8 to +0.5	Vdc
Operating Voltage	V _{DD}	36	Vdc
Maximum gate current	lgs	2	mA
Storage Temperature Range	Tstg	-65 to +150	°C
Case Operating Temperature	Tc	+150	°C
Operating Junction Temperature	TJ	+225	°C
Fable 2. Thermal Characteristics			
Characteristic	Symbol	Value	Unit

Thermal Resistance, Junction to Case by FEA	Balo	10	°C /W
T _C = 85°C, at Pdiss=7W	Rejc	10	-0.700

Table 3. Electrical Characteristics (TA = 25℃ unless otherwise noted)

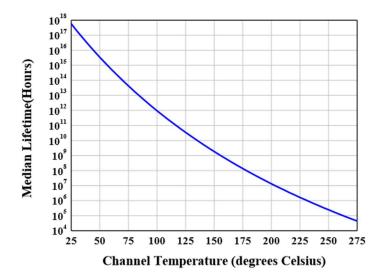
DC Characteristics (main path, measured on wafer prior to packaging)

Characteristic	Conditions	Symbol	Min	Тур	Max	Unit
Drain-Source Breakdown Voltage	VGS=-8V; IDS=2mA	V _{DSS}		200		V
Gate Threshold Voltage	VDS =10V, ID = 2mA	V _{GS(th)}	-4		-2	V
Gate Quiescent Voltage	VDS =28V, IDS=20mA, Measured in Functional Test	V _{GS(Q)}		-2.6		V

Ruggedness Characteristics

Characteristic	Conditions	Symbol	Min	Тур	Max	Unit
Load mismatch capability	5.8GHz, Pout=8W Pulsed CW					
	All phase,	VSWR		10:1		
	No device damages					

Figure 2: Median Lifetime vs. Channel Temperature





Typical performance

Figure 3: Efficiency and power gain as function of Pout

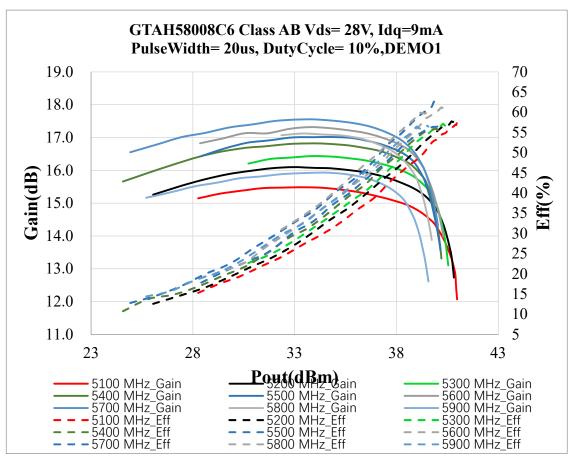
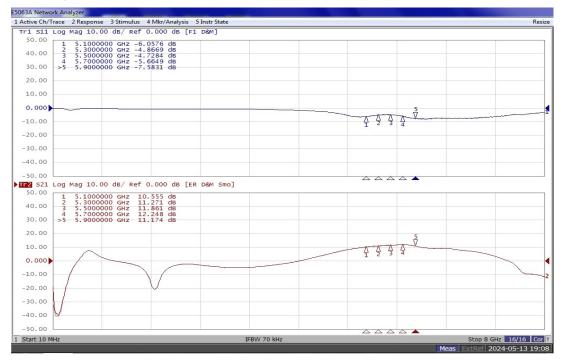
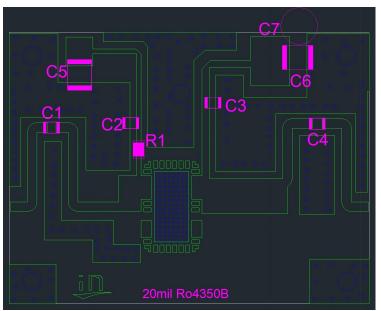


Figure 4: Network analyzer output S11/S21



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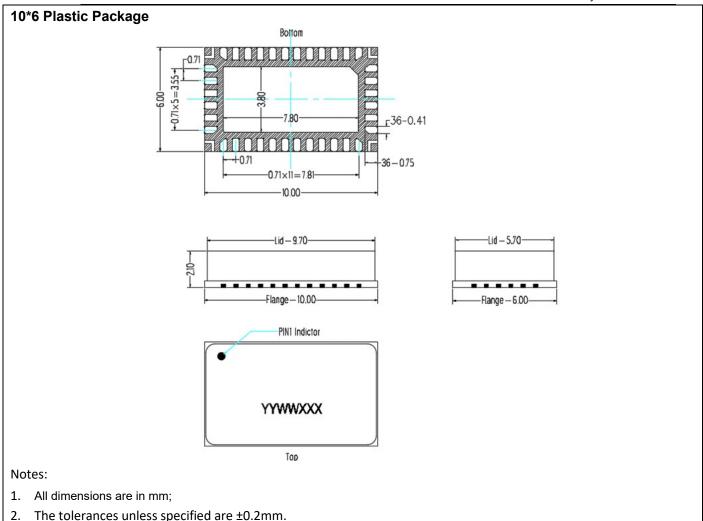
Figure 5: Picture of application board



Component	Value	Quantity
U1	GTAH58008C6	1
C1、C2、C3、C4	3.3pF	4
C5、C6	10uF/63V	2
C7	470uF/63V	1
R1	10Ω	1

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Revision history

Table 4. Document revision history

Date	Revision	Datasheet Status
2024/5/14	V1.0 Preliminary Datasheet Creation	

Application data based on: CWZ-24-09

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