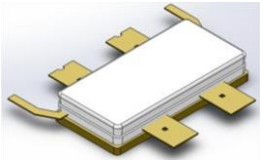
Gallium Nitride 50V, 750W,2.3-2.4GHz RF Power Transistor

XTAV24750B4VC



Description

The XTAV24750B4VC is a 750-watt, internally matched GaN HEMT, designed for 5G cellular applications with frequencies from 2.3-2.4GHz, **enabled by wide band VBW capability to support IBW up to 100MHz.**.

It can be configured as asymmetrical Doherty for 4G or 5G application, delivering 100W average power, according to normal 8.5dB back off.

There is no guarantee of performance when this part is used in applications designed Outside of these frequencies.

- Typical Doherty Pulsed CW and 1C W--CDMA Characterization Performance:

V_{DD} = 50 Vdc, I_{DQA} = 270 mA, V_{GSB} = –4.5Vdc,

(1)Pulsed condition: 20us and 10%

(2)1C WCDMA; Signal PAR = 10 dB @ 0.01% Probability on CCDF.

Freq (GHz)	Pulse CW Signal(1)			P_{avg}=50.0dBm WCDMA Signal(2)		
	P3 (dBm)	P4 (dBm)	P4 (W)	Gp (dB)	η_D (%)	$ACPR_{5M}$ (dBc)
2.30	58.50	59.01	796	13.80	53.60	-28.80
2.35	59.10	59.20	831	14.13	53.70	-29.88
2.40	59.01	59.03	800	14.50	53.10	-30.08

Recommended driver: Doherty (1 stage discrete solution): STAV27070C6

Applications

- Asymmetrical Doherty amplifier within B40 4G band
- S band power amplifier

Important Note: Proper Biasing Sequence for GaN HEMT Transistors

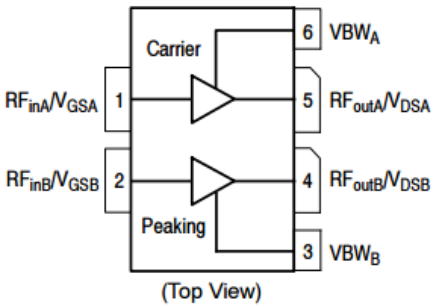
Turning the device ON

1. Set VGS to the pinch—off (VP) voltage, typically –5 V
2. Turn on VDS to nominal supply voltage
3. Increase VGS until IDS current is attained
4. Apply RF input power to desired level

Turning the device OFF

1. Turn RF power off
2. Reduce VGS down to VP, typically –5 V
3. Reduce VDS down to 0 V
4. Turn off VGS

1: Pin Connection definition



(Top View)

Table 1. Maximum Ratings

Rating	Symbol	Value	Unit
Drain—Source Voltage	V_{DSS}	+200	Vdc
Gate—Source Voltage	V_{GS}	-8 to +0.5	Vdc
Operating Voltage	V_{DD}	55	Vdc
Maximum gate current	Igs	92	mA
Storage Temperature Range	Tstg	-65 to +150	°C
Case Operating Temperature	T_C	+150	°C
Operating Junction Temperature	T_J	+225	°C

Table 2. Thermal Characteristics

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Case by FEA T_C= 85°C, Pout=100W, 2.6GHz Doherty application board	$R_{\theta JC}$	0.9	°C /W

Table 3. Electrical Characteristics (TA = 25℃ unless otherwise noted)

DC Characteristics (main path, measured on wafer prior to packaging)

Characteristic	Conditions	Symbol	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	VGS=-8V; IDS=38mA	V_{DSS}		200		V
Gate Threshold Voltage	VDS =10V, ID = 38mA	$V_{GS(th)}$	-4		-2	V
Gate Quiescent Voltage	VDS =50V, IDS=280mA, Measured in Functional Test	$V_{GS(Q)}$		-2.7		V

DC Characteristics (peak path, measured on wafer prior to packaging)

Characteristic	Conditions	Symbol	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	VGS=-8V; IDS=70mA	V_{DSS}		200		V
Gate Threshold Voltage	VDS =10V, ID = 70mA	$V_{GS(th)}$	-4		-2	V
Gate Quiescent Voltage	VDS =50V, IDS=500mA Measured in Functional Test	$V_{GS(Q)}$		-2.7		V

Ruggedness Characteristics

Characteristic	Conditions	Symbol	Min	Typ	Max	Unit
Load mismatch capability	2.4GHz, Pout=100W WCDMA 1 Carrier in Doherty circuit All phase, No device damages	VSWR		10:1		

Figure 3: Efficiency and power gain as function of Pout

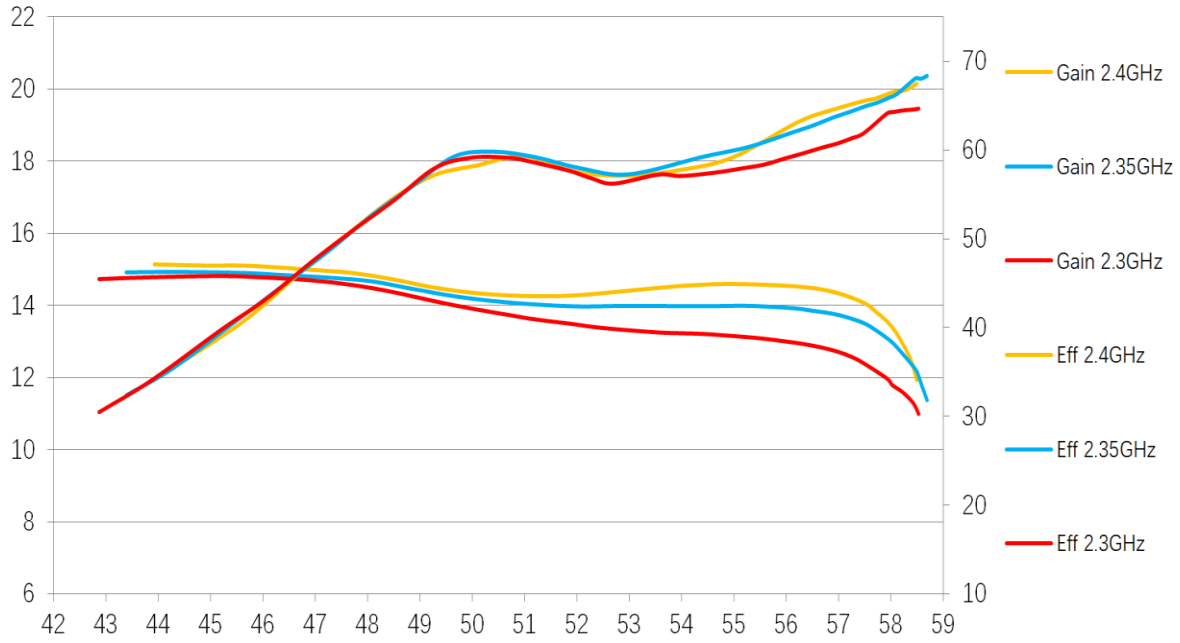


Figure 4: Network analyzer output, S11 and S21

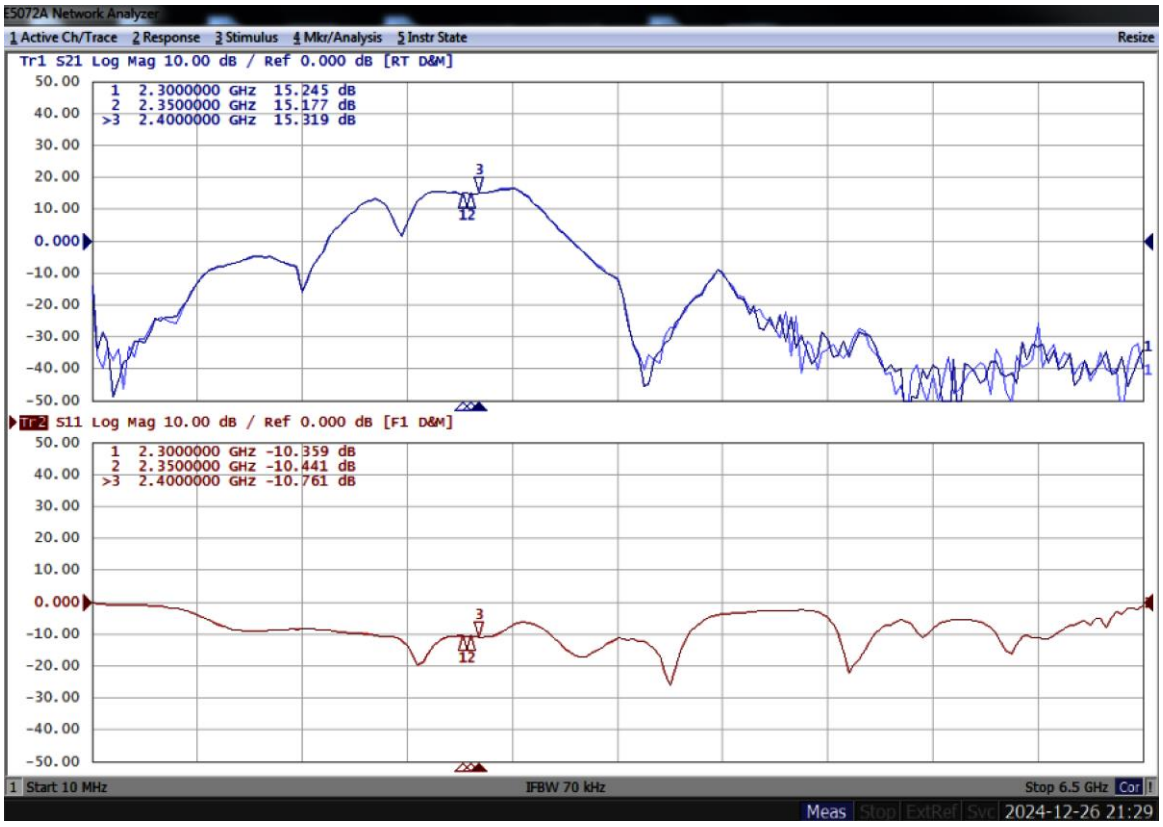


Figure 5: Picture of application board Doherty circuit

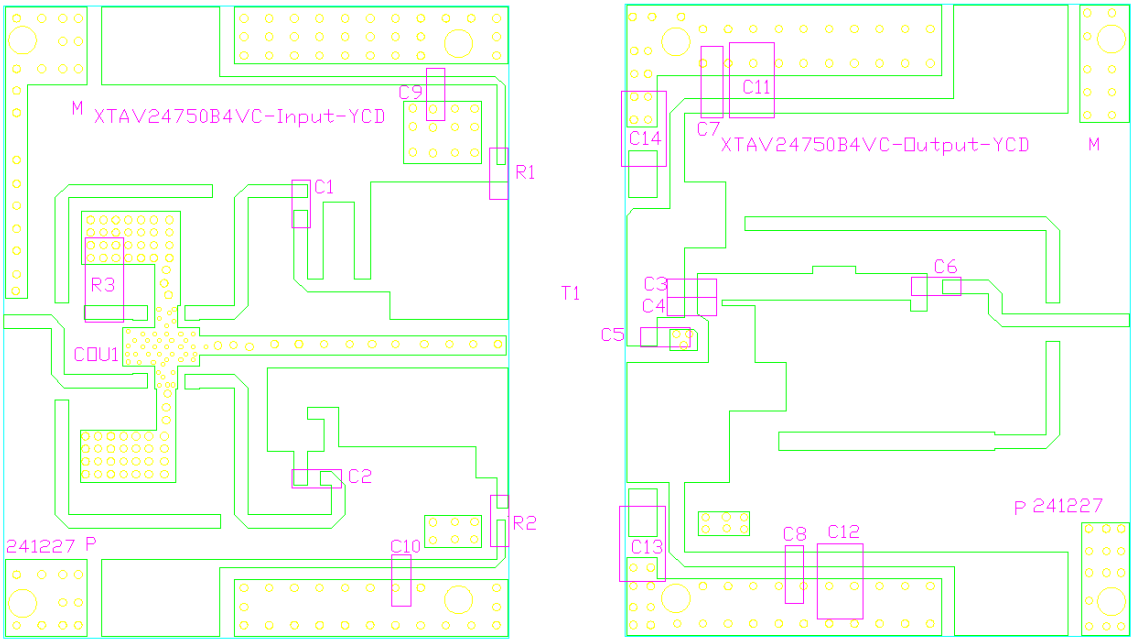
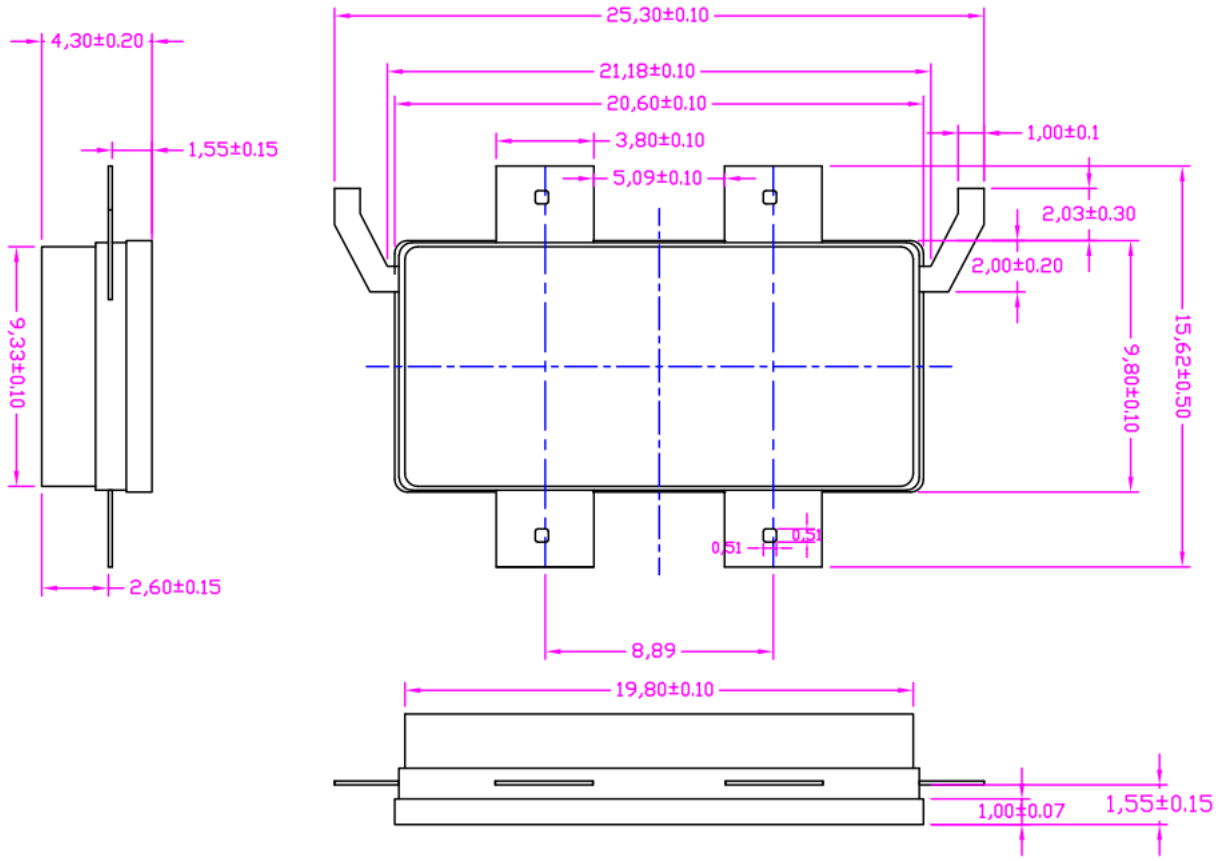


Table 4. Bill of materials of application board (PCB layout upon request, RO4350B 20mils)

Part	Quantity	Description	Part Number	Manufacture
C1,C2,C6,C7,C8,C9,C10	7	10pFHigh Q Capacitor	251SHS100BSE	TEMEX
C3,C4	2	2.2pFHigh Q Capacitor	251SHS2R2BSE	TEMEX
C5	1	1.8pFHigh Q Capacitor	251SHS1R8BSE	TEMEX
C11,C12,C13,C14	4	10uF MLCC	GRM32EC72A10	Murata
R1,R2	2	10Ω Power Resistor	ESR03EZPF100	ROHM
R3	1	51Ω Power Resistor	2512	RN2
COU1	1	3 dB Bridge	X3C26P1-03S	Anaren
T1	1	750W GaN Dual Transistor	XTAV24750B4VC	Innogration

Earless Flanged Ceramic Package; 6 leads- B4VC



Revision history

Table 4. Document revision history

Date	Revision	Datasheet Status
2024/12/27	**V1.0**	**Advanced Datasheet Creation**

Application data based on LWH-24-48

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