#### Document Number: XTAH15140GX Advanced Datasheet V1.1

### GaN HEMT 28V, HF-1.5GHz 140W, RF Power Transistor Description

The XTAH15140GX is a 140W GaN HEMT, designed for multiple application up to 1.5GHz

It can be used in CW, Pulse and any other modulation modes. There is no guarantee of performance

when this part is used in applications designed Outside of these frequencies.

• Typical class AB 400-500MHz RF Performance with device soldered

Vds=28V, Idq=100mA, CW

Voltage(V)	Pin(dBm)	Psat(dBm)	Psat(W)	Gain(dB)	Eff(%)
28	35	>51	120~150	16-17	65-70

Typical class AB 840-1020MHz RF Performance with device soldered

Vds=28V, Idq=100mA, CW

Voltage(V)	Pin(dBm)	Psat(dBm)	Psat(W)	Gain(dB)	Eff(%)
28	36	>50.5	115-150	15-16	60-66

### Applications

- L band power amplifier
- P band power amplifier
- ISM/RF Energy power amplifier

### Important Note: Proper Biasing Sequence for GaN HEMT Transistors

### Turning the device ON

- 1. Set VGS to the pinch--off (VP) voltage, typically –5 V
- 2. Turn on VDS to nominal supply voltage
- 3. Increase VGS until IDS current is attained
- 4. Apply RF input power to desired level
- Turning the device OFF
- 1. Turn RF power off
- 2. Reduce VGS down to VP, typically -5 V
- 3. Reduce VDS down to 0 V
- 4. Turn off VGS

### Table 1. Maximum Ratings

Rating	Symbol	Value	Unit
DrainSource Voltage	V <sub>DSS</sub>	+200	Vdc
GateSource Voltage	$V_{GS}$	-8 to +0.5	Vdc
Operating Voltage	V <sub>DD</sub>	50	Vdc
Maximum gate current	lgs	36	mA
Storage Temperature Range	Tstg	-65 to +150	°C
Case Operating Temperature	Tc	+150	°C
Operating Junction Temperature	TJ	+225	°C

### **Table 2. Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Case by FEA	<b>D</b> elo	1 25	°C /W
T <sub>c</sub> = 85°C, at Pdiss=60W	Rejc	1.25	-C /vv

### Table 3. Electrical Characteristics (TA = 25°C unless otherwise noted)

### DC Characteristics (measured on wafer prior to packaging)

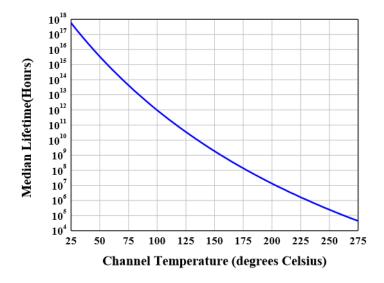
Characteristic	Conditions	Symbol	Min	Тур	Max	Unit
Drain-Source Breakdown Voltage	VGS=-8V; IDS=36mA	V <sub>DSS</sub>		200		V
Gate Threshold Voltage	VDS =10V, ID = 36mA	V <sub>GS(th)</sub>	-4		-2	V



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Gate Quiescent Voltage	VDS =28V, IDS=100mA, Measured in Functional Test V <sub>GS(Q)</sub> -3.24			V		
Ruggedness Characteristics						
Characteristic	Conditions	Symbol	Min	Тур	Max	Unit
Load mismatch capability	1.5GHz, Pout=140W Pulsed CW					
	All phase, VSWR 10:1		10:1			
	No device damages					

#### Figure 2: Median Lifetime vs. Channel Temperature



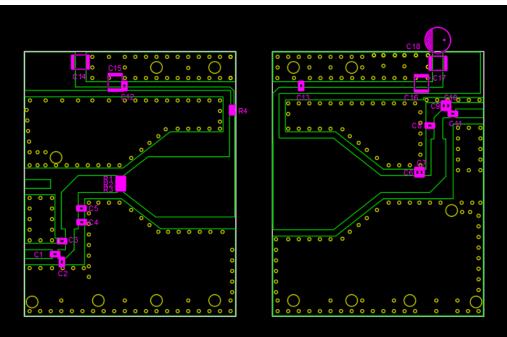
0.4-0.5GHz Typical performance





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### Figure 4: Picture of application board



Reference	Footprint	Value	Quantity
C1	0603	3 20pF	
C2, C3, C5, C10	0603 5.6pF		4
C4, C7	0603	10pF	2
C6	0603	12pF	1
C8	0603	0.5pF	1
C9	0603	4.7pF	1
C11, C12	0603	82pF	2
C13	0603	100pF	1
C14, C15, C16, C17	1210	10uF/63V	4
C18		470uF/63V	1
R1, R2, R3, R4	0603	0603 10ohm	
U1	GX	GX XTAH15140GX	

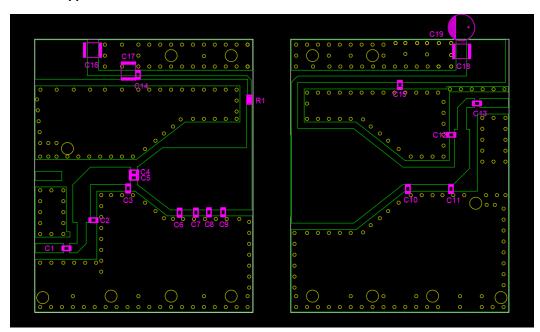


0.85-1GHz Typical performance

#### Figure 5: Network analyzer output S11/S21



Figure 6: Picture of application board



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Reference	Footprint	Value	Quantity
C1, C3	0603	6.8pF	2
C2	0603	2.2pF	1
C4, C5	0603	34pF	2
C6, C8	0603	2.7pF	2
C7	0603	8.2pF	1
C9, C10	0603	3.6pF	2
C11	0603	1pF	1
C12	0603	1.5pF	1
C13, C14, C15	0603	68pF	3
C16, C17, C18	1210	10uF/63V	3
C19		470uF/63V	1
R1	0603	10ohm	1
U1	GX	XTAH15140GX	

### Package Outline

### Flanged ceramic package; 2 leads

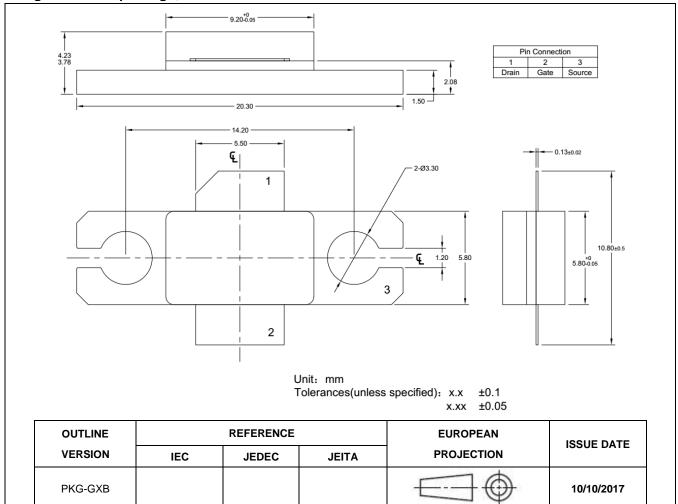


Figure 1. Package Outline PKG-G2E

### **Revision history**

#### Table 4. Document revision history

Date	Revision	Datasheet Status
2025/1/2	V1.0	Advanced Datasheet Creation

Application data based on: ZYX-25-01/02

### Notice

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